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FEEDBACK IN LOUDSPEAKERS

by R Conell

Electrical feedback is the backbone of many an electronic circuit. Acoustic feedback is not nearly so common, but R Conell suggests some ways of experimenting with it in a low-frequency loudspeaker.

Ever since Thiele and Small published their work on loudspeaker theory, it has been possible to calculate fairly accurately what the ideal enclosure is for a certain type of loudspeaker, or conversely how a loudspeaker will behave in a certain enclosure. According to Small, a closed box will behave as a second-order high-pass filter, while Thiele shows that bass reflex and transmission line boxes act as fourth- or sixth order filters. From this it is clear that a closed box will give better bass reproduction than an open system.

The performance of a filter is determined by its quality factor $Q$ and its resonant frequency $f$. This is also true of a complete loudspeaker system, including the enclosure, when the total $Q$ is designated $Q_e$ and the resonant frequency $f_e$. In an ideal bass system, these quantities should have values as follows:

$$Q_e < 0.5 \text{ to } 0.7, \text{ and } f_e < 30 \text{ Hz}.$$  Moreover, the volume of the enclosure should preferably not exceed 100 litres; the frequency range should be greater than 300 Hz; and the distortion should not exceed 1%.

It is virtually impossible to meet these requirements with a passive speaker system, particularly as regards $Q_e$ and $f_e$. In an active system, it is far easier to approach the ideal. Frequency response equalization is one way to tackle the problem. Basically, it is better, however, to make use of a controlled system. Unfortunately, such a system is prone to spurious oscillations, which can, however, be obviated by negative feedback.

**Basic controlled system**

Control is possible by converting some of the acoustic output of the loudspeaker into an electrical signal and returning this to the input of the power amplifier. To this end, a low-mass acceleration pick-up has to be fitted to the cone of the drive unit.

The block schematic of a possible arrangement is shown in Fig. 1. The left-hand box contains the control electronics, followed by the power amplifier, which has a gain of about 30 dB, and the loudspeaker system.

The control electronics consist of an adder that combines the left- and right-hand signals, a low-pass filter with a cut-off frequency of 100 Hz, and a difference amplifier where the filtered input signal is reduced by the correction signal from the feedback loop. The power amplifier can be of any type, but its gain should preferably be about 30 dB. A smaller gain would require some adjustment of the control loop, while a higher gain increases the tendency to oscillations in the loudspeaker system.

The loudspeaker system contains the drive unit, fitted with the acceleration pick-up, $M$, and an impedance converter, $IC_1$.

**Impedance converter**

The impedance converter—see Fig. 2—consists of a Type TL071 operational amplifier. Its pin-out is shown in Fig. 3. This stage should be fitted as close as possible to the acceleration pick-up, preferably direct onto the chassis of the drive unit as shown in Fig. 7.

**Control circuits**

Adder $IC_1$ in Fig. 3 combines the two stereo signals into a monaural signal. Potentiometer $P_i$ sets the input level for low-pass filter $IC_2$. This Bessel filter has a cut-off frequency of 100 Hz and a roll-off of 48 dB/octave. A similar filter was described in the January 1986 issue of *Elektor India*.

The control amplifier proper is formed by $IC_1$; the values of $R_n$, $R_m$, and $C_1$ determine the transient response of the overall system. These values will be reverted to under "Setting up." The control signal is deduced from the filtered audio signal in subtractor $IC_2$. The output of this stage is fed to buffer $IC_3$ via two low-pass sections, $R_s-C_t$ and $R_{st}-C_{st}$. These sections further suppress any tendency to oscillation and are absolutely necessary.
It is possible to omit impedance converter IC1 and buffer IC4, but the values of the low-pass sections between IC6 and IC9 should then be recalculated with due account of the input impedance of the power amplifier.

Modifying the drive unit

The acceleration pick-up is made from a piezo tweeter from which the chassis has been removed as shown in Fig. 4. The connexion wires have been cut at the terminals, not at the crystal and the remaining cone is then cut to the same size as the piezo disc. The resulting acceleration pick-up may be fitted over or under the dust cap of the woofer. The latter method is preferable, but only possible if the dust cap has been fastened with a thermoplastic glue. The cap may then be removed quite easily with a heated knife as shown in Fig. 5. The removal of the cap should, of course, be carried out with the greatest care to avoid damage to the cone of the drive unit or its speech coil. Once the dust cap has been removed, it should be stiffened with a thin layer of epoxy resin and a piece of glass fibre cloth at its base—see Fig. 6. The epoxy resin may be used at the same time to fix the pick-up in place. In the mean time, the woofer should be kept upside down to prevent dust entering the air gap.

After the epoxy resin has hardened, a thin flexible wire should be soldered to each of the two short connexion of the pick-up. These wires should also be glued to the dust cap to prevent them vibrating in unison with the cone later. Next, the dust cap can be fastened onto the cone again, preferably with thermoplastic glue to enable removal at a later stage if necessary. Before gluing it in place, however, pierce a small hole in the cone through which the flexible wires are fed. These wires should be glued to the cone in the same way as those to the speech coil. Finally, they should be connected to the impedance converter board as shown in Fig 2 and

Fig. 3 Circuit diagram of the control electronics

Fig. 4 Piezo tweeter after its chassis has been removed

Fig. 5 Removing the dust cap from the cone of the bass drive unit
40 pF.

1.5% volumes is measured.

0.6%

1.9

The loudspeaker drive unit should be strengthened on its sides with a thin layer of epoxy resin, which can be used at the same time to fix the acceleration pick up.

Table 1

<table>
<thead>
<tr>
<th>Frequency (Hz)</th>
<th>30</th>
<th>40</th>
<th>70</th>
<th>100</th>
</tr>
</thead>
<tbody>
<tr>
<td>Without feedback</td>
<td>4.5%</td>
<td>1.7%</td>
<td>0.65%</td>
<td>0.65%</td>
</tr>
<tr>
<td>With feedback</td>
<td>1.5%</td>
<td>0.6%</td>
<td>0.5%</td>
<td>0.65%</td>
</tr>
</tbody>
</table>

Maximum sound pressure at 40 Hz with different enclosure volumes

<table>
<thead>
<tr>
<th>Volume (litre)</th>
<th>50</th>
<th>70</th>
<th>100</th>
</tr>
</thead>
<tbody>
<tr>
<td>Without feedback</td>
<td>96 dB</td>
<td>100 dB</td>
<td>102 dB</td>
</tr>
<tr>
<td>With feedback</td>
<td>101 dB</td>
<td>103 dB</td>
<td>105 dB</td>
</tr>
</tbody>
</table>

System parameters measured in a 70 l enclosure

<table>
<thead>
<tr>
<th></th>
<th>Qk</th>
<th>E</th>
<th>f0 dB</th>
</tr>
</thead>
<tbody>
<tr>
<td>Without feedback</td>
<td>1.9</td>
<td>48 Hz</td>
<td>29 Hz</td>
</tr>
<tr>
<td>With feedback</td>
<td>0.6</td>
<td>17 Hz</td>
<td>20 Hz</td>
</tr>
</tbody>
</table>

Fig. 7 They should preferably be of about the same length as those to the speech coil.

The drive unit is then ready for operational use—see Fig. 7.

Setting up

All the constituent parts of the system should now be interconnected as shown in Fig. 1. Short out R1 and C1 with the aid of a switch to disable the control circuit. When the switch is opened momentarily, one of three things will happen:

- The loudspeaker remains quiet.
- The system oscillates at a low frequency (<100 Hz).
- The system oscillates at a high frequency (>1 kHz).

In the first case, everything is in order and the system can be taken into use.

In the second case, the connections from the pick-up to the impedance converter board must be reversed.

In the third case, the oscillations must be damped by changing the values of a few components. First, increase C1 to 1nF and, if this does not help, C1 to 1µF. If that still does not cure the problem, reduce the value of R1 and increase that of C1. Resistor R1 affects the lower cross-over frequency, while C1 alters the Qk of the system. The author has built several of these systems and has never encountered oscillation problems. Do not forget to remove the switch from across R1 and R1.

Finally

The frequency characteristics in Fig. 8 show the results of the modification: it is quite evident that the lump between 30 and 100 Hz in the response of the system disappears when the feedback is introduced. The response between 30 and 30 Hz is also much improved.

A number of pertinent measurements are tabulated in Table 1.

The system with feedback was also compared with a number of top quality loudspeaker systems in all cases, it performed equally well over the bass range, in spite of its cost being only a fraction of that of the competition.
ELECTRONIC POTENTIOMETERS
by T Scherer

An exploratory look at all-electronic replacements for potentiometers in high quality AF applications.

Potentiometers are, arguably, not the best way of controlling the volume and tone settings in an AF amplifier. We all know that they can cause scratching noises when operated, collect dust, and sometimes develop contact problems giving rise to troublesome discontinuities in the operative range. High quality potentiometers for AF applications are not only difficult to obtain, but also notoriously expensive. In the following sections we will briefly examine a number of low-cost alternatives to potentiometers used in various circuit sections of AF equipment.

The carbon track potentiometer
This most commonly used voltage divider is generally composed of a carbon film deposit on a ceramic base arranged in a three-quarter circular form (270°). The poor contact definition of the wiper on this thin carbon film readily gives rise to scratching noises made audible in the loudspeakers. Furthermore, dust and foreign particles can easily enter the potentiometer enclosure, and block certain sections of the carbon track, so that the amplifier falls still at particular volume settings, making the adjustment very difficult. Stereo potentiometers of the carbon film type are a further source of trouble. With most inexpensive types, the tolerance on synchronicity of the set resistance is often no less than 20%, even with linear law types. The voltages at the wipers of a logarithmic stereo potentiometer can also differ by some 20%, causing a volume difference between the channels of a maximum of 2 dB, which may be noticeable in listening. Potentiometers are generally mounted on equipment front panels, and are connected to the electronic circuit with the aid of shielded wires that often

![Diagram](image-url)
carry very low signal level at relatively high impedance. This makes the amplifier susceptible to noise, hum and strong RF fields, which can still be picked up by the carbon track in the potentiometer (plastic enclosures), and even in the cable shield.

In conclusion, it is reasonable to say that the standard carbon track potentiometer is unsuitable for a great many critical applications.

Stepping switches

Rotary (wafer) switches with fixed resistors at the contacts are, in principle, a good way to effect volume and tone setting in an amplifier. The tracking is adequate, and scratching noises due to spindle movement are effectively ruled out. However, many rotary switches of suspect quality do develop contact problems after protracted use. A major difficulty in the designing with stepping switches is the finding of types having the number of positions required to ensure a sufficiently smooth adjustment range.

Wire-wound potentiometers

Long ago in the history of electronics, all potentiometers and resistors were made from resistance wire. For a number of specific applications, the wire-wound potentiometer is still in use. Ganged types with motor drive units can be found in some of the most expensive types of amplifier. This application, however, requires sophisticated mechanical engineering on the one hand, and a fairly complex electronic control circuit on the other, making the whole setup rather cumbersome and expensive at the same time.

An LDR-based potentiometer

The first attempts at making a fully electronic potentiometer were carried out with combinations of LDRs (light dependent resistor) and a small bulb. Although the results were quite satisfactory for AF equipment on the market in the early 1960s, we would nowadays reject the LDR-and-bulb control for incorporation in Hi-Fi equipment, in view of the noise production, rumble sensitivity, and poor tracking characteristic of the stereo versions.

We all know that each and every electronic component remains subject to continuous enhancement by the joint force of manufacturers and their research laboratories. The German firm Heimann, for instance, took up the long forgotten LDR for further research, and used two of these devices together with a LED to make an opcoupler that has adequate features for Hi-Fi applications. The LDRs in their Types LT10xx and LT20xx opcouplers are of excellent quality, and especially the LT20xx should do very well as a stereo potentiometer with adequate tracking properties—see Fig. 1a for the pinning and R1a curves, and Fig. 1b for a suggested application circuit.

An OTA-based potentiometer

A fairly simple potentiometer replacement can be realized with the aid of an OTA (operational transconductance amplifier), which is essentially an amplifier with current-controlled gain. The gain range of about 80 dB, the extensive usable frequency range and linearity of the current-gain correlation, make an OTA such as the Type LM3600 eminently suitable for the applications we are concerned with here.

Those who want to experiment with these devices will find the suggested circuit in Fig 2 of use for further experiments. The only drawback associated with OTAs is their limited dynamic range, which results in a maximum attainable signal-to-noise ratio of about 80 dB.

Analogue multiplexers

The circuit shown in Fig 3 is a high quality, all-electronic volume control featuring 16 dB and 2 dB steps as controlled from a 6-bit digital input. The ICs in this circuit are the well-known Type 4051 eight-channel analogue multiplexer/demultiplexer, which in essence is an electronic version of an 8-way, single pole rotary switch. The outputs are 0-7, the pole is output 8, and the switch position is set with the 3 bits at the A/B/C inputs. Examples: applying binary code 010 to the A/B/C inputs of the left-hand multiplexer connects input 2 (pin 15) to output 2. The input signal for opamp A1 is therefore taken from the –32 dB contact on the resistor ladder. The resistors at the inputs of the second multiplexer driving A2 are dimensioned.
sioned to give 2 dB attenuation steps, so that the overall range of this electronic potentiometer is from 0 to −96 dB as set with 6 bits. A balance control can be made with two of these circuits, operated on the basis of software.

The tone control section shown in Fig. 4 uses the same principle as the above volume adjustment. The resistors as part of the R-C filters in the feedback loop of A1 are selected with 3-bit codes for bass and treble.

Use high-stability resistors and capacitors when constructing these circuits, and provide ample decoupling of the supply lines. The opamps should be low-noise types such as the TL074 indicated in the circuit diagram. The digital adjustment of the volume and tone control circuits is a matter we leave in your hands. You may want to use an up/down counter, a microprocessor port, or a special switch to arrange for the correct bit combinations at the multiplexer control inputs (consult Table 1).

Table 1

<table>
<thead>
<tr>
<th>MAIL STATES</th>
<th>&quot;ON&quot;</th>
</tr>
</thead>
<tbody>
<tr>
<td>CHANNEL(S)</td>
<td></td>
</tr>
<tr>
<td>INHIBIT C B A</td>
<td></td>
</tr>
<tr>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>0</td>
<td>0</td>
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<td>0</td>
<td>0</td>
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<td>0</td>
<td>1</td>
</tr>
<tr>
<td>0</td>
<td>1</td>
</tr>
<tr>
<td>0</td>
<td>1</td>
</tr>
<tr>
<td>0</td>
<td>1</td>
</tr>
<tr>
<td>1</td>
<td>X</td>
</tr>
</tbody>
</table>

Fig. 3. A 6-bit high quality volume control circuit that uses CMOS analogue multiplexers.

Fig. 4. Using electronic switches instead of a potentiometer in a tone control circuit.

5.24 ektor mouse may 1997
SECONDARY BREAKDOWN IN POWER TRANSISTORS

by Sue Can & Ray Ashmore

This article examines the different types of secondary breakdown that occur in power transistors, and investigates the phenomena that cause them. It concludes that secondary breakdown is a function of transistor technology, and cannot always be improved without some trade-off in other parameters.

One of the basic failure mechanisms in power transistors is secondary breakdown. This term includes various physical phenomena which are completely different. They depend on the different use of transistors in the circuits and have in common the electrical and thermal instability inherent in transistors themselves.

The conduction behaviour of an emitter-base junction and the current gain of a transistor depend significantly on the temperature and increase as a function of the temperature. Electrical and thermal instabilities may act simultaneously within the device, thereby giving rise to destructive secondary breakdown mechanisms.

An understanding of this mechanism is of great importance for a safer optimum application of a power transistor.

A distinction should be made between direct second breakdown (D/SD) which is distinguished by a normal direction of base current (entering into an NPN transistor) and inverse second breakdown (RBSOA), when the base current is in the opposite direction (extracted from an NPN transistor). The limits to which a transistor can be used without entering into RBSOA are defined by the reverse bias safe operating area (RBSOA).

Direct second breakdown

It is important for the power circuit designer to know the locus of the Ic-Vce points defining the boundary between stable and unstable operation of forward biased transistors. This locus defines the SOA safe operating area, that is, the area of the log Ic-log Vce plane which may be used without any risk in DC current conditions or with different width pulses at a known temperature. A typical SOA is shown in Fig. 1. The limits of this area are as follows:

1) The A-B section represents the upper limit of the collector current that may normally be used, generally limited by wire bonds. Operation at higher currents may cause damage to the wires of their bonding.

2) The C-D section is the -I slope curve section (i.e., the section with constant dissipation) defined by:

\[ V_{CEO} = P_{max} = (T_{max} - T)/R \]

This section therefore indicates the maximum dissipable power of the device. \( T_{max} \) is the maximum temperature which the collector-base junction may reach, over which the device reliability may be compromised. In power transistors, \( T_{max} \) varies between 125 and 200 degrees Celsius and generally depends on the metallurgy and the type of package. \( R_P \) is the thermal resistance between the collector-base junction and the case, including all the silicon and package system. The increase in the maximum dissipable power when the pulse width decreases (Fig. 1) corresponds to the decrease of \( R_P \) with respect to \( R_g \).

3) Section C-D is the limit due to the second breakdown phenomenon (or D/SD) and limits the maximum power that the transistor can dissipate. This may occur even at relatively low Vce voltages.

4) Section D-F is the limit due to the thermal instability of the transistor. The main causes of this instability are:

- The \( V_{CE} \) of a directly biased base-emitter junction, at constant current, decreases linearly with temperature, with a \( 1-2 \) to \( 2.5 \) mV/°C slope. The base-emitter voltage of the transistor may therefore be expressed by:

\[ I_B = I_C \exp \left( \frac{v_{BE}}{kT} \right) \]

and, when \( V_{CEO} \) is kept constant, it increases with temperature.

- The hfe at the relevant voltage values increases as a function of temperature according to the law:

\[ hfe = hfe_0 \exp \left( \frac{AEg}{kT} \right) \]

Where \( AEg \) is an activation energy which is a feature of the transistor.

- The thermal conductivity of silicon decreases when temperature rises, causing a worsening of the thermal resistance of the transistor.

When these three phenomena are taken into consideration, it may be observed that a pulse of power \( P = V_{CEO} \) generates:

- An increase of the junction temperature, giving rise to an increase of \( I_C \) and \( hfe \), and therefore to an increase of \( I_C \) with a following increase of \( P \) and, therefore, a further temperature increase.

- Dissipation to the external environment, controlled by the thermal resistance \( R_P = dT/dP \) which tends to stabilize the device.

The situation evolves towards stability when:

\[ \frac{\Delta E_g}{\Delta T} \geq \frac{3I_C V_{CEO}}{P} \]

is smaller than \( 1 \), or instability if \( >1 \).

In this way, a stability factor, \( S \), may be defined that will be a function of \( V_{CEO} \) and \( I_C \):

\[ S = R_P V_{CEO} \frac{3I_C}{P} \]

Figure 1 Safe operating areas which may be used without any risk in DC current conditions or with different width pulses at a known temperature.
When $S > 1$, so called "thermal runaway" occurs and the junction temperature increases without any limit, thereby degrading and possibly damaging the transistor. The failure generally occurs when the surface temperature becomes greater than the eutectic temperature between silicon and the contact metal (front aluminium) with a consequent melting of the alloy. A localized temperature increase may also damage the crystal, or the inner temperature of the device may reach values high enough to melt the silicon.

To understand $I_{eb}$ phenomena which give rise to a reduction of the maximum power that the transistor can dissipate as $V_{ce}$ increases (zone D-E), it is necessary to take into account that device operation is not homogeneous on all the dice area. There are disuniformities in the emitter base current density that may be due to junction disuniformities, crystal defects and, most of all, to the emitter edge concentration phenomenon.

The voltage drop due to the base current flowing through the cross resistance $r_{bb'}$ gives rise to a disuniformity of $V_{ce}$ at the junction, and therefore to the disuniformity of the current density $j_e$ (see Fig. 2).

A side drop of 26 mV reduces the injected emitter current by a factor 1/e, where $e$ is the base of the natural system of logarithms ($= 2.71828 \ldots$).

A concentration of the current at the emitter periphery is therefore generated, so the active silicon area is reduced and hot spots occur, leading to an effective increase of the thermal resistance. As a result, the maximum dissipable power is decreased.

When $V_{ce}$ is increased, the effect of the base-collector electric field is to increase the base current concentration. Different techniques may be adopted to limit the $I_{eb}$ phenomenon. Fundamentally, these consist of minimizing the mechanism that triggers electrical and thermal instabilities in the transistor. The basic techniques are:

1. minimization of crystal damages, metal impurities, and doping disuniformities;
2. optimization of package and die attach techniques to minimize the thermal resistance on which the stability factor $S$ depends. Disuniformities of subicon die bondings to the case may give rise to adverse variations of $R_e$ as a macroscopic parameter for the dice as a whole, but also to significant variations between different points, giving rise to premature second breakdown;
3. increase of the base thickness to reduce the high current densities (due to emitter crowding) flowing through the collector base junction (where the electric field is localized), so that the density of the dissipated power is decreased. High base thicknesses, however, will result in lower cut off frequencies and slower switching times;
4. optimization of the horizontal geometry.
5. introduction of distributed base-ballast resistances connected in series with the base, the emitter or both, which tend to give a negative feedback to thermal runaway, therefore stabilizing the device.

The introduction of a ballast resistance in series with the base of the emitter may reduce from $j_b$ to $j_b'$ the current density in the hot spot. The emitter ballast resistance is generally obtained by opening emitter contacts thinner than the emitter strip. In this way it is possible to limit the current density at the boundaries of the emitter. These resistances show the drawback of increasing the saturation voltage of the transistor by the amount $V_{ce} = \text{Rel.}$

On the other hand, the base-ballast resistance is obtained through a "N +" pocket (in the case of NPN), around the emitter area. This $N^+$ diffusion, being unbiased, cannot be traversed by the base current, so this is forced to flow below the $N^+$ through a small section and, in the case of a diffused base, encounters a higher resistance on the way to the edge of the emitter. In this way it is possible to increase $I_{eb}$ significantly.

It should be noted that the SOA limits are temperature dependent and suitable derating must be applied.

Reverse second breakdown

The reverse breakdown phenomenon (Ebr) is also due to thermal and electrical instability of the transistor. As already mentioned, it is distinguished from $I_{eb}$ by the presence of a reverse $I_{eb}$ (i.e., with a direction opposite to the normal direction of a transistor operating in the active zone) and by high $V_{ce}$ values of the transistor. The device may be in these working conditions during turn-off with an inductive load.

In Figure 3 the common emitter characteristic curves for a transistor are shown.

It is easy to understand the behaviour of these curves when the common emitter gain expression is considered

$$|\text{Av}| = \frac{A_{FE}}{(1/A_F)}$$

for high $V_{ce}$ values, $A_F$ is replaced by $M_{AFE}$.

For low $V_{ce}$ values, $M$ is an insignificant factor, being very close to 1; it increases when $V_{ce}$ is increased according to the following expression:

$$M = 1/[(1-V_{ce}/V_{CEO})^2]$$

From expressions (5) and (6) it is clear that $I_{be}$ depends on $V_{ce}$, becoming infinite when $M_{AFE} = 1/V_{CEO}$.

The negative slope section, which is a feature of the curves with $I_e < 0$, is due to the fact that $A$ decreases at low values of the emitter current.

During turn-off with an inductive load, the transistor has to operate with negative base current and a high value of $I_c$. It often has to reach a working area above $V_{CEO}$, remaining there all the time required for the inductance to be discharged. Fig. 4 shows the behaviour of $I_c$, $V_{ce}$, $I_e$ and the power dissipated by the transistor during turn-off.

The area of the dissipated power corresponds to the energy stored by the inductance $\frac{1}{2}LI^2$, which is discharged into the transistor and this is called the second breakdown energy ($E_{br}$).

Like $I_{eb}$, the voltage drop due to the reverse $I_e$ flowing through the side resistance $r_{bb'}$ makes the node of the emitter strip more biased than its periphery. In this way, a current concentration occurs at the emitter centre.

Let us analyse the case of an NPN transistor with diffused base and epitaxial collector, i.e., with constant concentration ND of donors doping particles.

Poisson's equation is...
The $X$ axis is normal to the silicon device surface, $(x)$ is the charge per unit volume, $E$ is the dielectric constant of silicon. When the collector current is limited to low values, expression (7) becomes ($q$ being the electron charge):

$$dV/dx = qN_0/x$$

and the electric field behaviour is similar to that shown in figure 5 for $J_c = J_f$.

The voltage $V_{CE}$ ($=V_{Cox}$) is given by the area of the $E/X$ graph and is smaller than primary breakdown voltage due to the reaching of critical field $E_c$. In the presence of significant values of current density $J_c$, the expression (8) is modified due to the $n$ concentration of electrons flowing at the speed $v$ through the depletion layer

$$dE/dx = qnNd/x$$

where $n = J_c/qV$

At constant $V_{CE}$, the area limited by $E$ has to remain constant. When $J_c$ increases, the $E/X$ slope varies ($J_3$) until its sign is changed ($J_2$) and $E_c$ is reached ($J_1$). At this point avalanche local multiplication of electrons occurs with an uncontrolled current increase—and so a stop is formed with a very high temperature that gives rise to either crystal damage or silicon melting. Possible crystal defects, metal ions, and junction disuniformities further exacerbate this phenomenon. The avalanche multiplication is very fast and very localized so the device remains externally cold. The $E_{ab}$ behaviour is not influenced by the die bonding quality. High $E_{ab}$ values can be obtained with a proper geometric design to limit the current crowding and, most of all, by inserting a second epitaxial layer $N$ of intermediate doping between the collector and the substrate.

The intermediate layer creates the condition shown in Fig. 6. When the current density increases ($J_2$) the electric field at the interface $N/N^+$ is increased. Before the critical field $E_{cr}$ is reached at the interface, the contribution of layer $N$ becomes significant in sustaining the voltage. A further density increase ($J_3$) reduces the electric field at the interface $N/N^+$ and the breakdown is not triggered until the critical field is reached at interface $N/N^+$. For a good power transistor with $V_{CEOUS} = 450V$ the current density $J_{f}$, corresponding to $E_{cr}$ is of the order of 20A/mm², i.e., greater by a factor 10 when compared to the average current density given by the ratio between maximum saturation current and emitter area.

The $E_{ab}$ behaviour is also influenced by the conditions outside the transistor, $R_{he}$, $V_{BE}$, $L$. The base conditions are especially important, as they regulate the crowding phenomenon.

The system most commonly used by power designers to reduce the $E_{ab}$ effect during turn off with inductive load is a clamping or 'snubber' circuit, that limits the voltage peak between collector and emitter.

The presence of the clamping circuit allows only a minimal amount of the energy stored in the inductance to be absorbed by the transistor and $E_{ab}$ becomes independent of the value of $L$ and practical RSBOA limits may be defined.

The presence of high $V_{CE}$ and negative base current, $I_b$ may give rise at high current to the previously described $E_{ab}$ phenomenon, even in the presence of the clamping circuit. The multi-epitaxial transistors show a better behaviour even in the presence of a clamp.

The reverse bias safe operating area establishes the maximum switchable current with inductive load versus clamping voltage in very harsh base conditions that simulate the real base driving conditions in the circuits.

The temperature is not a major factor in the $E_{ab}$ and so the RSBOA rating can be considered to be independent of temperature.

**Conclusion**

Second breakdown performance is a function of transistor technology and cannot always be improved without some trade-off in other parameters. The application conditions have a considerable effect on both $E_{ab}$ and $E_{cr}$ capability.
PRESET EXTENSION FOR FUNCTION GENERATOR

by M. Kistingler

A simple to build, ten-frequency preset unit for the Elektor Function Generator that features an adjustable sweep function, a LED indication, and much more at a very small outlay.

The AF Function Generator described in Elektor India, January 1983, has generated a lot of interest, which is mainly due to the instrument being versatile, relatively simple to construct, and sufficiently accurate for a great many applications. The preset extension proposed here is a separately housed, 10-way programmable ancillary intended to drive the generator's VCO input. Frequencies that are often used for test and measurement purposes can be called up at the flick of a switch, and there is also a facility to successively select all ten of them at variable speed, providing a 10-frequency sweep function. Furthermore, the extension provides an output signal to trigger an oscilloscope with any one of the ten available frequencies. Ease of control is the key word in this design. Once you have set the ten generator output frequencies with the aid of multimeter presets, you can select manual operation on the extension and press the slow step key until the relevant frequency is enabled, as indicated by the associated LED. If the Manual switch is in the Auto position, the VCO voltages are successively output at a rate defined with the speed potentiometer and the fast/slow push-button switch. A BCD (thumbwheel) switch is used to select the period of one of the 10 available VCO voltages for triggering an oscilloscope.

Standard components are used throughout this extension, which will quickly prove an indispensible add-on unit that can save you quite some time in setting the generator's output frequency.

Circuit description

The circuit diagram of the proposed extension is shown in Fig. 1. At the lower left is the power supply, which delivers +5 V for the logic circuits, and +10 V for the sweep oscillator. IC1 and the VCO output drivers T1:T4. The latter voltage is provided by a precision regulator Type LM317 (IC5) to ensure the stability of the ten VCO drive levels. +5 V supply is conventionally based on a Type 7805 regulator which can easily handle the 150 mA current demand of the (LS)TTL circuits.

With 

set to MAN, depression of single step push-button S1 causes N1 and delay network R1-C1 to provide a trigger pulse to the B input of monostable multivibrator IC5 whose output period is defined with R1-C1. When S1 is open, the pulse at output Q of IC5 is passed through gates N2 and N3 and fed to the clock input of counter IC1. If S1 is in the Auto position, N3 blocks the single step pulses from IC5, and IC1 is arranged to be fed from oscillator IC2 via level translator T6. Potentiometer P1 and fast/slow push-button switch S1 allow precise setting of the VCO sweep speed. Note that S1 is actually part of the speed potentiometer, so that turning this fully counter-clockwise automatically enables manual selection of the direct voltage level from the preset extension, and hence of the function generator's output frequency.

Counter IC1 is advanced by pulses from N4, and the BCD code at its QA-Q7 outputs is applied to the XOR gates in IC4, as well as to BCD-to-decimal decoder IC2. The Type 74LS80 counter is set up to count from 0 to 9, and is reset to state 0 at power-on with the aid of C1-R4. The trigger signal for the oscilloscope is obtained from N12-N14 and N15, which function as a 4-bit comparator in conjunction with IC4 and a BCD switch for selection of the relevant trigger pulse. The output of N14 goes high if the logic state of outputs QA-QD on IC1 matches that of the A-D lines on the BCD switch. Any one of the 10 outputs of decoder IC1 can enable an associated driver stage, whose direct output voltage is defined with a multturn preset. If, for instance, output 9 of IC1 goes low, the output of open-collector inverter N5 goes high. Transistor T6 is turned on, LED D1 lights, and a portion of the emitter voltage is led to the VCO input of the lownchon generator, via the wiper of P1, and summing diode D10. The circuit around T1 serves to raise the ground potential of the extension so as to increase the active range of the presets in the analogue output stages. It should be noted that this arrangement makes it impossible to feed the preset extension from the generator's

<table>
<thead>
<tr>
<th>Parts list</th>
</tr>
</thead>
<tbody>
<tr>
<td>Resistors 1 ± 5%:</td>
</tr>
<tr>
<td>R1, R2 = 47 kΩ; 1%</td>
</tr>
<tr>
<td>R3 = 100 kΩ</td>
</tr>
<tr>
<td>R4 = 5.6 kΩ</td>
</tr>
<tr>
<td>R5, R6 = 10 kΩ incl. = 100 kΩ</td>
</tr>
<tr>
<td>R7 = 1 kΩ</td>
</tr>
<tr>
<td>R8, R9 = 10 kΩ incl. = 10 kΩ</td>
</tr>
<tr>
<td>R10 = 220 kΩ</td>
</tr>
<tr>
<td>R11 = 270 kΩ</td>
</tr>
<tr>
<td>R12 = 100 kΩ</td>
</tr>
<tr>
<td>P1 = 500 kΩ linear potentiometer with SPST switch (6s)</td>
</tr>
<tr>
<td>P2 = 5kΩ preset</td>
</tr>
<tr>
<td>P3 = 2.1 kΩ incl. = 4.7 kΩ multturn preset</td>
</tr>
</tbody>
</table>

| Capacitors: |
| C1, C2 = 100 nF |
| C3 = 220, 25 V |
| C4 = 100 nF, 10 V |
| C5 = 47, 63 V |
| C6 = 100 nF, 40 V |
| C7 = 47, 63 V |
| C8 = 100 nF, 40 V |
| C9 = 220, 25 V |
| decoupling capacitors (100 nF) as required |

| Semiconductors |
| B1 = 8051500 |
| D1, D2 = 1N4148 |
| D3, D4 = 1N4007 |
| IC1 = 741 |
| IC2 = 74121 |
| IC3 = 74L590 |
| IC4 = 74LS590 |
| IC5 = 74LS42 |
| IC6 = 74LS20 |
| IC7 = 74LS24 |
| IC8 = 74LS30 |
| IC9 = 74LS42 |
| IC10 = 74LS60 |
| IC11 = 74S163 |
| IC12 = LM317T |
| IC13 = 500 mW heat sink |
| IC14 = LM317T |
| IC15 = BC5408 |
| IC16 = BC5407 |
| IC17 = BC5488 |

Miscellaneous: |
| F1 = 100 mA, delayed action. |
| Fuseholders for F1 |
| T1 = 15 V, 200 mA |
| S1/S2 push to make button |
| S3 = part of P1, |
| S4 = SPST mains switch |
| Suggested enclosure: Veroboard Type 75-3007C (180 x 120) |
| 40 mm prototyping board (Veroboard) as required |

BCD Thumbwheel switch

5.28 wattor India May 1987
supply. Also, observe that the pulse level at the sync out terminal is 5 \( V_{PP} \) with respect to the extension ground potential, not that of the function generator. LED \( D \) serves the double purpose of raising the base potential of \( T \) and functioning as the on/off indicator of the preset extension.

**Construction and setting up**

The proposed extension circuit is readily built on a piece of Veroboard and housed in an ABS enclosure that can be placed on top of the function generator or the associated sweep generator. Although not shown in the circuit diagram, the supply lines to the logic circuits should be decoupled with 100nF capacitors. Keep the wires to the switches and the sweep potentialmeter as short as possible. The frequency indication LEDs can be fitted in a neat row on the front panel, complete with numbers 1-10 for easy reference.

After building the circuit, it is suggested to adjust the output voltage of IC1. Use a DMM and set \( P_1 \) for a reading of 10.00 V. Turn \( P_1 \) to AUTO and check whether operation of \( S_1 \) causes the LEDs to light in succession. Turn \( P_1 \) to AUTO and check whether the sweep speed can be adjusted with \( P_3 \) and \( S_1 \). If necessary, adapt \( C_5 \) or \( C_6 \) to define the sweep speed. Turn \( P_1 \) back to \( \text{MAN} \) and use a DC coupled oscilloscope to see whether the VCO voltages are all stable and free from digital noise and ripple.

Finally, connect the extension to the VCO input on the function generator, and adjust the 10 presets for the test frequencies you require.

**Function generator:** Elektor India, January 1985

**Sweep generator:** Elektor India, December 1985.
Fig 1 shows the amplitude and phase shift behaviour of a Butterworth filter, and Fig 2 those of a Linkwitz-Riley network. Note the 3 dB peak of the Butterworth filter. This cannot be obviated by increasing the separation of the cross-over frequencies of the low- and high-pass sections, because this would violate the first requirement of zero phase shift between the outputs. For clarity’s sake, the two characteristics are combined in Fig. 3 to highlight the difference between them.

The Linkwitz curve is rather more rounded in the vicinity of the crossover frequency, and starts falling off somewhat earlier. The slightly different phase shift of the two filters should also be noted.

The foregoing discussion is true only if the signals are sinusoidal. The pulse (or step) response of the Linkwitz filter causes the same problems as that of a Butterworth filter, assuming that both filters have separate low and high-pass sections. Even a Linkwitz filter is therefore not perfect.

A practical filter

A Linkwitz filter may be designed as a passive or as an active type. The circuit diagram of an active design is shown in Fig. 4: this may be constructed on the printed-circuit board shown in Fig. 5. Note that this board is identical to that used for the electronic crossover network published in the September 1984 issue of *Elektor Electronics*.

The circuit of Fig. 4 is for a three-way loudspeaker system. The network has cross-over frequencies of 500 Hz and 5,000 Hz and roll-offs of 24 dB per octave. Stage A1 serves as a buffer for the input signal, but this is split three-way. The low-pass section is formed by A6 and A5; the middle-frequency section by A4 and A3 (high) and A1 and A2 (low); and the high-pass section by A11 and A12. Each section is provided with a potentiometer for setting the level of the output signal (P1, P2, and P3, respectively), and a stage to buffer the output (A1, A3, and A5, respectively). The power supply lines are stabilized by voltage regulators IC8 and IC9. The cross-over frequencies may be altered with the aid of Table 1 (any frequency) or Table 2 (the 12 most likely frequencies). The values in Table 2 have deliberately not been rounded off to the nearest.

**Fig. 1.** Butterworth network: amplitude and phase characteristics over the audio frequency range. The fat line represents the sum of the outputs of the filters.

**Fig. 2.** Linkwitz network: amplitude and phase characteristics over the audio frequency range. The fat line represents the sum of the outputs of the filter sections.

**Fig. 3.** Butterworth and Linkwitz characteristics combined to highlight their differences. The networks used had a slope of 24 dB per octave.
The sections may also be given a slope of 12 dB per octave by using A6, A8, A9, and A12 as buffers. Resistors R10, R11, R15, and R16, as well as capacitors C15, C18, C23, and C26, are then replaced by wire links, while R14, R15, R2, R3, C13, C14, C21, and C22 are omitted.

The circuit may be adapted for use with a two-way system by the omission of the entire middle-frequency section, except for A1 which is housed in the same package as A4.

If the slope is changed to 12 dB per octave, the connections to one of the loudspeakers must be reversed, because the phase shift at the cross-over frequency is 180° here. In a three-way system, this should be done at the middle-frequency speaker, in a two-way system at the tweeter.

A passive filter may be constructed as shown in Fig. 6. The values of the actual components used should be as close as possible to the calculated ones, otherwise the filter will become a cross between a Linkwitz and a Butterworth.
If the filters are given a 12 dB per octave slope, the connections to the middle-frequency loudspeaker (in a three-way system) or those to the tweeter (in a two-way system) should be reversed.

The loudspeaker impedance must be corrected in a manner that ensures it is constant and ohmic at the cross-over frequency. The corrected impedance of the loudspeaker, $R_{l}$ in Fig 6a and 6b, should be ascertained as detailed in Loudspeaker Impedance Correction (Elektor India, June 1986).

**Fig. 6. Passive Linkwitz sections (a) with a 12 dB per octave slope, and (b) with a 24 dB per octave slope.**

**Table 1**

<table>
<thead>
<tr>
<th>Type</th>
<th>Low-pass section 12 dB/octave</th>
<th>Low-pass section 24 dB/octave</th>
</tr>
</thead>
<tbody>
<tr>
<td>$C_{a} = C_{b} = 1/2 nF R_{b}$</td>
<td>$C_{b} = C_{D} = 1/2 nF R_{D}$</td>
<td></td>
</tr>
<tr>
<td>$R = 4.7 , 10 , k\Omega$</td>
<td>$R = 4.7 , 10 , k\Omega$</td>
<td></td>
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</table>

**Table 2**

<table>
<thead>
<tr>
<th>$R = 5k\Omega$</th>
<th>$R = 5k\Omega$</th>
<th>$C = 4n7$</th>
<th>$C = 4n7$</th>
<th>$R = 4k\Omega$</th>
<th>$R = 4k\Omega$</th>
</tr>
</thead>
<tbody>
<tr>
<td>$f (Hz)$</td>
<td>$C_{a} = C_{b} (nF)$</td>
<td>$C_{a} = C_{b} (nF)$</td>
<td>$C_{a} = C_{b} (nF)$</td>
<td>$R_{a} = R_{b} (k\Omega)$</td>
<td>$R_{a} = R_{b} (k\Omega)$</td>
</tr>
<tr>
<td>100</td>
<td>284</td>
<td>261</td>
<td>204</td>
<td>330</td>
<td>330</td>
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<tr>
<td>200</td>
<td>142</td>
<td>107</td>
<td>100</td>
<td>169</td>
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<tr>
<td>300</td>
<td>71.1</td>
<td>50.2</td>
<td>40.2</td>
<td>113</td>
<td>113</td>
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<tr>
<td>400</td>
<td>56.8</td>
<td>33.5</td>
<td>28.7</td>
<td>64.7</td>
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<tr>
<td>500</td>
<td>47.4</td>
<td>25.1</td>
<td>23.8</td>
<td>67.7</td>
<td>67.7</td>
</tr>
<tr>
<td>600</td>
<td>40.6</td>
<td>20.1</td>
<td>19.9</td>
<td>66.4</td>
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</tr>
<tr>
<td>700</td>
<td>35.5</td>
<td>16.3</td>
<td>15.2</td>
<td>48.4</td>
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<tr>
<td>800</td>
<td>28.4</td>
<td>13.4</td>
<td>11.3</td>
<td>42.3</td>
<td>42.3</td>
</tr>
<tr>
<td>900</td>
<td>21.8</td>
<td>10.6</td>
<td>8.8</td>
<td>33.9</td>
<td>33.9</td>
</tr>
<tr>
<td>1,000</td>
<td>18.9</td>
<td>8.0</td>
<td>6.7</td>
<td>22.6</td>
<td>22.6</td>
</tr>
<tr>
<td>1,500</td>
<td>14.2</td>
<td>5.2</td>
<td>4.1</td>
<td>16.3</td>
<td>16.3</td>
</tr>
<tr>
<td>2,000</td>
<td>11.4</td>
<td>4.0</td>
<td>3.1</td>
<td>13.5</td>
<td>13.5</td>
</tr>
<tr>
<td>2,500</td>
<td>9.47</td>
<td>3.0</td>
<td>2.3</td>
<td>11.3</td>
<td>11.3</td>
</tr>
<tr>
<td>3,000</td>
<td>8.12</td>
<td>2.2</td>
<td>1.9</td>
<td>9.88</td>
<td>9.88</td>
</tr>
<tr>
<td>3,500</td>
<td>7.11</td>
<td>1.9</td>
<td>1.6</td>
<td>8.47</td>
<td>8.47</td>
</tr>
<tr>
<td>4,000</td>
<td>6.88</td>
<td>1.6</td>
<td>1.4</td>
<td>6.77</td>
<td>6.77</td>
</tr>
<tr>
<td>5,000</td>
<td>5.88</td>
<td>1.3</td>
<td>1.2</td>
<td>3.39</td>
<td>3.39</td>
</tr>
<tr>
<td>10,000</td>
<td>2.94</td>
<td>1.0</td>
<td>1.0</td>
<td>2.39</td>
<td>2.39</td>
</tr>
</tbody>
</table>
RIMS: COUNTING ATOMS

by Dr Kenneth W. D. Ledingham, Department of Physics and Astronomy, University of Glasgow

Resonant Ionisation Mass Spectroscopy (RIMS) is a unique, ultra-sensitive on-line technique which can detect down to the level of a few atoms. It is applicable to any sample, whether solid, liquid or gas and can be used to assay every element in the periodic table apart from helium and neon, as well as any stable or radioactive isotope. It is likely to find important applications in fundamental and applied physics, and to become a valuable tool in the semiconductor industry and in diagnostic medicine.

The need to develop new analytic ways to measure ultra-trace quantities of elements in various substances is becoming urgent in many branches of science, engineering and medicine. There are already many sensitive analytic techniques, including neutron or photon activation analysis, inductively coupled plasma spectroscopy, atomic absorption and various kinds of mass spectroscopy, particularly secondary-ion mass spectroscopy (SIMS). The sensitivity of these techniques for trace analysis is usually limited to the order of parts in \(10^8\) or \(10^9\).

In the last few years problems have arisen that require ultra-trace analysis at the previously unheard-of sensitivities of parts in \(10^9\) to \(10^{-7}\) or even further. Already, three areas which require such analysis have been identified and as techniques are developed, even more applications are likely to become apparent.

Firstly, it is essential to reduce the minimum detection limit of impurities in silicon if improvement, especially in miniaturisation, of the semiconductor manufacturing process is to be maintained. Secondly, Professor M. Baxter of the Scottish Universities Research and Reactor Centre, near Glasgow, has speculated whether there is a health risk from the presence of very low activity beta emitters in the environment. They are very difficult to monitor because they are likely to be below the sensitivity range of conventional nuclear counter techniques.

Finally, the presence of trace amounts of certain elements in human body fluids and tissues is considered to be essential to health. This is a poorly understood branch of biochemistry and many divergent figures for trace metal concentrations in apparently healthy people have been published. But there is growing evidence that many of the studies are flawed by gross analytic inaccuracies and that new, reliable techniques are necessary at sensitivity levels of parts in \(10^8\).

During the middle and late 1970s the possibility of applying laser techniques of single-atom detection to ultra-trace analysis attracted interest. The technology had been pioneered largely by Professor V. S. Letokhov of the Academy of Sciences in Moscow and Professor G. S. Hurst of Oak Ridge National Laboratory, USA. Resonant Ionisation Spectroscopy, RIS as it has come to be known, can detect one atom of a specific type in a background of \(10^4\) others in gaseous phase. The implications of this degree of sensitivity for many disparate fields of research are likely to be enormous.

Resonant Ionisation Spectroscopy

With the development of intense, tunable, pulsed lasers the simultaneous absorption of several photons by a single atom or molecule to produce a free electron and a positive ion became experimentally feasible. In the simplest RIS process a pulsed laser is tuned precisely to the wavelength required to excite the atom or molecule from its ground state of energy to an excited state that is unique to the element under study. A second photon, of the same wavelength and from the same laser pulse, interacts with the atom in its excited state and causes an electron to be released from it, thereby creating a positive ion. This process can be made more selective by adding further resonant steps in the excitation process, using a second laser tuned to another frequency. Five different laser schemes, represented in the first illustration, can release all the elements in the periodic table, except helium and neon. From left to right in the diagram they are:

1. \(A(a,a_i)A^+\)

   This reaction means that two photons of the same wavelength (that is, with angular velocity \(\omega\)) create the ion pair.

2. \(A(a,a_i)A^+\)

   The laser wavelength is frequency doubled into the ultra-violet and then mixed with the fundamental to create the ion pair.

3. \(A(a_i,\omega)A^+\)

   In this process, three photons are absorbed with two colours being involved, indicated by \(\omega\) and \(\omega_i\).

4. \(A(a_i,\omega)A^+\)

   One colour is frequency doubled (2\(\omega_i\)) and another photon of a second colour is absorbed as well as one of the original photons.

5. \(A(a_i,a_i)A^+\)

   In this case, usually three photons of the same colour are absorbed to create the ion pair.

The second diagram is the periodic table of elements with one of the five schemes being applied to each. After Professor Hurst, in the early days of the technology, the electrons created in the resonance process were detected by on-line proportionality counters. Soon, however, it became obvious that the ultra-trace isotope selectivity needed was too, so mass spectrometers were introduced to detect the positive ions. Although both magnetic and quadrupole mass spectrometers have been used by different research groups, the arrangement preferred now includes a time-of-flight mass spectrometer.

Resonant Ionisation Mass Spectroscopy

When laser techniques are used to detect ultra-trace amounts of elements or isotopes in a substance or matrix, three separate steps are involved. A typical laser time-of-flight mass spectrometer is shown in the third illustration, indicating the steps. Firstly, a pulsed, charged, argon beam or a neutral argon beam, ablates or creates neutral atoms from the surface of the solid sample to be assayed. Ideally, the atoms created should be accurately representative of the solid under analysis and to date argon ablation has shown to be largely matrix-free. This technique is now considered to be superior to the laser ablation technique, which is a high-temperature method known to cause matrix problems because it favours the easily
vapourable materials. SIMS, already mentioned, also uses an ion beam to ablate the surface of the sample but analyses only the charged ions, which are created in numbers some two or three orders of magnitude fewer than the neutrals. Because they are charged, these ions are emitted at a rate that is a function of the chemical composition of the surface. RIMS and SIMS are made quantitative by making comparisons with well-characterised standard samples, so if there are any matrix problems, any quantitative analysis is likely to be inaccurate.

Having created a cloud of vapour above the target, RIS lasers then selectively ionise atoms of the chosen element in the vapour cloud, which are subsequently accelerated into the time-of-flight mass spectrometer. Secondary ions created by the ablation process can be rejected by electostatic fields or by varying the time between the ion beam pulse and the RIS laser pulse. The normal laser arrangement to achieve total elemental coverage is an Nd:YAG laser powering two dye lasers, one of which has frequency doubling facilities. Typical lasers of this kind operate with pulse lengths of several nanoseconds at repetition rates of some tens per second. The transverse spatial dimensions of the beam are typically a few millimetres. One of the strengths of RIS is that the photo-ionisation processes can be made almost 100 per cent efficient, that is, it reaches saturation. By saturation of the RIS process, we mean that every atom of a quantum selected species which was in its ground state before being subjected to the photon field of a pulsed laser is converted to a positive ion and a free electron during the short duration of the laser pulse. Because saturation occurs when laser fluences, by which we mean energy per unit area, are typically about 100 mJ cm\(^{-2}\), conventional commercial lasers require modest focusing of a 3-mm beam. It is hoped that RIMS will become a routine ultra-trace analytic technique, so a short analysis time is desirable, of the order of minutes. For this purpose the low repetition rate of Nd:YAG lasers (30 s\(^{-1}\)) is a limitation. Two of the severe limitations of conventional mass spectroscopy can be eliminated when tuned lasers are used to produce the ions for mass analysis. In a conventional mass spectrometer the ions to be analysed are normally electron-beam induced, so molecular interferences and isobaric effects cannot be avoided. A mass spectrometer cannot easily distinguish between CO and Ne, for example. This is a phenomenon known as molecular interference. Not all distinguish between \(^{40}\)Ca, \(^{40}K\) and \(^{40}Ar\) because they are isobaric; that is, they have similar masses. These ambiguities are avoided when RIMS is used. The final step of the RIMS tech-

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### Periodic Table of Elements

This table includes one of the five schemes of the first diagram described in each.

(After Professor Hurst)

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Technique is to count and measure the mass of the laser-induced ions, using a time-of-flight (TOF) mass spectrometer. A TOF instrument is a non-magnetic system in which the ions first accelerate through a series of closely spaced electrodes and then pass through a field-free region (D) of considerable dimensions, of the order of one metre, to be detected by an ion detector such as a channeltron. In its simplest form, the transit time (t) of the ion in the field-free region is proportional to the length of the field-free region and to the square root of the ion mass (m).

For an accelerating voltage of 1000 V, with D equal to 1 m, t is about 20 ns for a singly ionised mass of 100 atomic mass units.

There are several advantages to be gained by using a TOF mass spectrometer: firstly, entire mass spectra can be accumulated in a very short time and an entire spectrum can be recorded for each laser pulse; secondly, TOF systems measure isotopic ratios very accurately, because they measure them under identical conditions; finally, the accuracy of a TOF spectrometer depends on electron circuitry instead of extremely accurate mechanical alignment, so it is simpler to make. The time honoured disadvantage of TOF instruments is low resolution due to the poorly defined spatial and temporal character of conventional ion formation. But that scarcely applies when the ions are formed by lasers, because the laser spot has a tight focus and the laser pulse is so short, between 5 and 10 ns.

In the last year a number of groups in the USA, the Soviet Union and Europe have been set up to exploit the sensitivity of RIMS. Already it is claimed that the technique is capable of detecting impurities at the level of 1 part in 10^11 in a routine analysis time of 5 minutes.

Future Development

The design of the RIMS instrument so far described is by no means optimised. A number of promising lines of research have yet to be investigated which may lead to better sensitivity. Each of the three steps in the RIMS process will be considered, to see whether improvements are possible.

During the past few years, a great deal of attention has been paid to photon, electron and ion ablation of solids. At present, argon ablation of the sample is the most popular technique, though recent developments in metal ion beams such as those of gallium and caesium might increase the ion-sputtered yield per unit incident current. What is not in question is that these metal-ion beams can be focused to far smaller spots than an argon beam, down to submicron focal dimensions, so they are likely to be of great importance in future for precise scanning of sample surfaces. Over the next few years, it is improvement of the RIS step that is likely to contribute most to greater sensitivity. While an Nd:YAG pumped dye laser system has a repetition rate of 30 pulses s^-1, copper vapour lasers have recently been developed, in particular by Oxford Lasers (UK) which have a repetition rate of 6000 pulses s^-1, capable of pumping dye lasers to provide saturation intensities. Thus is likely to increase the efficiency of RIMS considerably, especially in detecting minute quantities of the actinides, recently demonstrated by Professor Kluge and Professor Trautmann of the University of Mainz. At present, however, there are electronic difficulties in handling data at such a large rate. The problems arise from not having enough storage capacity and from the transfer rates of available high-speed transient recorders.

One possible improvement in sensitivity may be understood by considering the stepped photo-ionisation process in the final diagram. In (a) an electron in its ground state absorbs a photon and is promoted to an excited state with a cross-section that is typically about 10^-14 to 10^-16 cm^2. Another photon is absorbed and the excited atom is ionised. The photo-ionisation step is characterised by a small cross-section of 10^-17 to 10^-18 cm^2 and therefore by a large laser fluence being needed to achieve saturation. The fluence is achieved by focusing, so that the volume of interaction with the ablation cloud is small. If, however, procedures (b) and (c) are adopted the probability of ionisation is greater by two or three orders of magnitude. In process (b) the atom is excited to close to the continuum (a Rydberg state) and then finally ionised with high efficiency using a pulsed electric field.

Another possibility of improvement is shown in (c) where the final ionisation step is to a so-called auto-ionisation state, above the ionisation level but having a large cross-section. Considerable research is necessary to identify the auto-ionisation states in a number of elements before this powerful procedure can be adopted. If processes (b) and (c) can be used then the saturation fluences of the laser are greatly reduced, so that the beam need not be focussed. The volume of interaction is then bigger.

Future Applications

One exciting aspect of this technology is that there are likely to be important applications in both fundamental and applied physics. In connection with fundamental physics, applications of RIMS to solar neutrino experiments, double...
betonic decay, baryon conservation and magnetic monopole searches as well as detection of quark atoms and superheavy atoms are being actively pursued. In particular, a detector based on the \(^{89}\text{Br} (v, e) ^{89}\text{Kr}\) to measure the \(^{89}\text{Be}\) neutrino source in the Sun has been shown to be feasible because the long-lived (2 x 10^6 year) \(^{89}\text{Kr}\) can now be counted with RIMS. In applied and commercial science, the applications of RIMS are likely to be very far reaching. In the semiconductor and electronic industries RIMS can identify impurities that restrict performance of high-speed, high-density integrated circuits. The technique can extend downwards the present minimum detection limits for contaminants by perhaps three orders of magnitude or greater. In the medical field, early diagnosis of certain diseases by using trace-element concentrations in body tissues and fluids is a very attractive possibility but must be carried out in a non-invasive way by using as small quantities of material as possible. Finally, RIMS can assist in selecting sites for storing hazardous nuclear wastes by using ground-water dating techniques as well as allaying public concern by ensuring that environmental monitoring be made as sensitive as possible.

0314/6

Advanced universal digital filter

A real-time universal digital filter, developed by Fern Developments Ltd for use in speech processing, audiology, psychoacoustics, electrophysiology, and geophysics, offers linear filtering capabilities which are said to be superior to those that can be achieved with conventional analogue techniques. The benchtop EF8, based on a design conceived by the Medical Research Council, is a S2-coefficient finite impulse response, non-recursive filter that offers an unlimited number of totally different filtering actions, the anti-alias (pre-process) and post-process sections use high precision programmable low pass filters. The filter unit has an operating bandwidth of 0-30 kHz, attenuation rates of typically 4000 dB per octave, and up to S12 weighting coefficients for symmetrical responses.

Fern Developments Ltd
7 Springburn Place
College Milton North
Glasgow G74 5NU

Grundig do it with robots

Helping Grundig on the road to success is a new robotic VCR production line which the company says is in advance of any other in the world today. Making VS400 machines, which will retail in the UK at around the £400 mark, the production line cost £3 million to install. It was designed and built entirely by Grundig engineers and took a mere six months to complete from putting pen to paper in the drawing office to the first complete machine coming off the 300-metre long production line.

When on full production, the automated plant is expected to produce at least one million VCRs per year, each one taking just 35 minutes to make plus another two hours in soak testing. Each machine goes through 87 work stations and through automatic quality tests on its way to completion. The new VCR line is just the first of a planned series of developments which will continue to radically change Grundig’s approach to video production:

Grundig International Ltd
42 Newlands Park
London SE26 5NQ

SMA assembly of PCBs

The WS1500 combination workstation from Surface Mounted Production Systems Ltd is intended for the surface-mounted assembly (SMA) of printed-circuit boards (PCBs). It incorporates a precision dispenser, vacuum pick-up, infra-red soldering unit, and a soldering iron. PCBs up to 7 x 4 inches can be accommodated. The WS1500 enables prototype design and development, single or small batch production and repair work on surface-mounted circuit to be carried out at one workstation. It is priced at less than £2000.

Surface Mounted Production Systems Ltd
Unit 5
Sandbank Industrial Estate
Dunoon PA23 8PB
WHERE ELECTRONIC MESSAGES HAVE THE EDGE

by Barry Fox

The new age of information technology is founded on one simple truth. It is quicker, easier, and cheaper to send pulses of electricity down a telephone wire or over a satellite link than it is to transport people or packages by road, sea or air. The telex service has until now been the standard means of sending text. Telex is a reliable war horse but has its own snags. The equipment is bulky and expensive, trained operators are needed to send messages, and the service relies on dedicated lines—that is to say special circuits designed to carry telex pulses rather than speech. It is still not widely recognized that almost every personal computer, either desk top or portable, can be used for electronic mail through one of the available services. It is the modern alternative to sending correspondence by telex. Text is sent from one computer to another along a conventional telephone line via a central message-handling computer. Already scriptwriters, translators, bankers, journalists, and lawyers are using electronic mail to send text from home to office. Sales teams use it to keep in touch with their headquarters while working round the country. Musicians use it while on tour. Electronic mail terminals can work equally well from an office desk or a hotel room far away.

Digital pulses

In its simplest form, a home computer sends messages either to the screen or to a printer. If it is programmed with additional communications software, it can send similar messages from its output—usually an RS-232 socket. This output is in the form of a stream of digital pulses, similar to telex, but much faster. They can be sent down a short wire cable to a matching computer system. This is how several computers are networked in an office. The pulses will not travel reliably down a conventional telephone line so they must first be converted into audible tones which the telephone network handles like speech.

A special device called a modem—short for modulator/demodulator—is needed to convert the computer pulses into sound tones. It is connected between the computer output and the telephone line socket, while a computer at the other end of the telephone line has a matching modem. This converts incoming tones back into digital pulses which are then displayed on the computer screen or printed on paper.

Four services

Electronic mail provides a mailbox system into which messages can be dropped by one user to be picked up later by another. A host computer handles the messages with a system of passwords to ensure that messages can only be picked up by the people to whom they are addressed. In Britain there are four electronic mail services. The most successful so far is telecom Gold which is run by British Telecom and has around 30,000 subscribers. Rival services are offered by Easylink—a subsidiary of Cable and Wireless; Comet from Isetel—a subsidiary of British Leyland; and One—to-One—a private company now owned by United States Telecom's company Telesis. Each of these services offers a message drop facility. When someone working from home or a hotel room wants to contact an office, he or she calls the relevant electronic mail telephone number and sends a message which is held in a message services computer. Later, the person at the office calls the same electronic mail number and reads the message off the computer. The text can be viewed on screen, stored on magnetic disk for subsequent word processing, or printed direct on to paper like a telex.

Any office wanting to use electronic mail should first find out what services are on offer. The Telecom Gold service in Britain is derived from the ITT Dialcom system developed in the United States of America. It is now used in over a dozen countries around the world, and is proving increasingly popular.

How to buy

Most businesses that decide to install an electronic mail system will find it cheaper. In the long run to buy the hardware and software through a dealer whose purchase price includes the cost of installing the equipment, getting it up and running, and teaching the staff how to use it. Once a system has been installed, staff may very soon wonder how they ever lived without it.
The supporting software for the programmer is an EPROM-resident block of Z80 machine code that provides a deluxe menu, help pages, a built-in test routine, and, of course, EPROM status information plus error reports.

The intelligent programming algorithm

As the holding capacity of their EPROMs increases, it is logical for manufacturers to develop programming methods that enable loading these devices within an acceptable time. Should the "old" 50 ms per address programming method apply to, say, a Type 27256 EPROM (32 K x 8), roughly half an hour would be needed for the device to be completely loaded. Intel, Fujitsu, National Semiconductor, and other leading EPROM manufacturers have, therefore, come up with various versions of an intelligent programming algorithm to speed up the loading process. As its name implies, this method relies on the use of a microprocessor, ruling out the possibility to use timers with a fixed output period for the generation of the programming pulses. The flowchart shown in Table 4 shows that the essence of the intelligent algorithm lies in the raising of Vcc from +5 V to +6 V, and the variable length of the programming cycle. The program-and-verify loop can only be left with the byte either correctly programmed, or still incorrect after a 25-pulse cycle. Therefore, with relatively few programming pulses required for a byte to verify correctly, the value of variable x is relatively low, and less time is needed for the address to be loaded. Following the variable number of programming pulses, an additional pulse of 3 ms ensures that programmed data bytes are absolutely stable in the EPROM. At this stage, an example might help to illustrate the workings of the algorithm.

A specific byte requires 9 pulses for it to be stored correctly in the EPROM. The programming cycle thus takes
\[(9 \times 1) + (3 \times 9) = 36\] ms.

Figure 8 illustrates that a programming cycle can be quite long. In fact, intelligent programming is not necessarily faster than normal (50 ms), fast-1 (20 ms), or fast-2 (10 ms) tuning arrangements, since the worst case cycle duration is
\[25 + (3 \times 25) = 100\] ms. However, you will soon find that newly purchased, intelligent EPROMs generally require only the minimum pulse time of 4 ms per address for reliable loading. Returning to the previously mentioned Type 27256, 3 minutes or so then suffice to completely load this device.

The intelligent programming methods adopted and recommended by Intel (intelligent programming, etc) and Fujitsu (Quick Pro™) differ only marginally as regards the duration of the programming pulse, the number of iterations before the EPROM is rejected as faulty, and the pulse multiplication factor. National Semiconductor's algorithm, however, is based on the use of 0.5 ms pulses, a maximum iteration of 20, no multiplier, and a Vcc level of 13 V instead of the more usual 12.5 V. Thus MSX EPROM programmers do not support National's algorithm, but nonetheless gives good results with their chips.

As could be expected, the tuning of the programming cycles is interrupt-based and jointly controlled by the CPU in the computer and the CTC in the I/O & Timer cartridge. The control program arranges for timer T5 in the CTC to provide the number of programming pulses required to successfully load a byte into an EPROM address. Iteration and pulse multiplication are effected in accordance with the flowchart shown in Table 4. Extensive tests have shown that the adopted algorithm gives satisfactory results with the vast majority of intelligently programmable EPROMs.

Although not expressly indicated in the flowchart, the control program and the CTC ensure that EPROM data and address lines are stable before any write action can take place. For this purpose, timer T5 in the CTC provides 4 µs long delays as detailed in last month's installment of this article.

Program description

An MSX compatible micro can have up to 4 primary slots, numbered 0, 1, 2, 3, each with a memory capacity of 64 Kbytes and subdivided in 4 pages of 16 Kbytes. It is also possible for a slot to be expanded, which means that it comprises four sub slots X.0, X.1, X.2 and X.3. In theory, therefore, there can be a maximum of 16 slots identified as 00 up to and including 3F. Since the Type 2920(A) CPU is an 8-bit microprocessor, its addressable memory area is 64 Kbytes, that is, four pages, but these can be part of any (expanded) slot. It is, for instance, possible for the system to operate with page 0 from slot 0, page 1 from slot 2, and pages 2 and 3 from slot 3. The absolute address ranges are thus: page 0 = 0000-3FFF, page 1 = 4000-7FFF, page 2 = 8000-BFFF, page 3 = C000-FFFF. Pages can be swapped and
Table 4. Intelligent programming of EPROMs essentially entails applying no more pulses than strictly necessary for correct loading

Fig. 8 This oscillogram shows that addresses may differ in respect of the number of programming pulses required for loading a byte. Upper channel address line A4; lower channel 18 x 71 - 26 ms for the first byte, (1 x 1) + 3 for the second and third

----- ELEKTOR MSX EPROMMER -----

**EPROM TYPE:** 27128
**PROGRAMMING VOLTAGE:** 12.5V
**EPROM BEGIN:** $688888$
**EPROM END:** $62FF$
**MEMORY BEGIN:** $6000$
**MEMORY END:** $7FFF$
**BLANK CHECK:** YES
**PROGRAM:** YES
**VERIFY:** YES
**READ AND RUN CHECK SUM:** NO
**DISPLAY ERROR:** NO
**PROGRAM MODE:** INTELL
**CHECK SUM:** $6000$
**ADDRESS COUNTER:** $63FF$

RESULT PROGRAMMED

---

Switched on and off by means of particular system commands, which will not be gone into in this article. Page 0 is usually reserved for the MSX BIOS (Basic Input/Output System), and page 3 for the system stack and scratch blocks, variables, the keyboard buffer, etc. At power-on, an MSX computer invariably examines pages 1 and 2 in all slots for the presence of EPROM-resident programs, which are immediately started if a particular identification code is found in the first 16 address locations. If such an identifier is not found, the BASIC ROM on page 1 is enabled, and the machine boots up accordingly.

The control program for the EPROM programmer comes in the form of a ready-programmed EPROM Type 27128 (16 Kbytes), available through our Readers' Services under number 552. This EPROM is inserted in the socket on the cartridge board for MSX computers, described in Elektor Computers, described in Elektor India, March 1986. In the following section we will set out how to correct all add-on units to make a functional set-up.

The programmer software immediately runs from page 1 at power-on. After completing the necessary initialization routines, the program finds out which slot has RAM in pages 1 and 2 for use as the EPROM data area (32 Kbyte maximum size, 4000-BFFF), it copies part of itself into the highest possible RAM area on page 3, that is...
it inserts itself between the stack and string & scratch blocks. After all this has been done, control is returned to the computer's normal start-up procedure, which means in most cases that BASIC will be started. The EPROM software can now be run by typing CALL EPROMx, where x is the cartridge address area, 1, 2, or 3. The program, when called, automatically selects the appropriate slot(s) for the RAM buffer, and then switches back to where it came from with the aid of routines on page 3. All switching between RAM and EPROM resident subroutines in the programmer is available to the user, and makes it possible for the proposed software to run on any MSX computer equipped with at least 64 Kbytes of RAM. Extensive use is made of vector-addressing, and all keyboard and screen input/output is routed via the BIOS on page 8. To make sure that data for or from the EPROM is not overwritten by the system stack, or possibly the RAM resident portion of the control program itself, it is a good idea to check whether there is enough room for your data by typing PRINT HEX(FREE+1;2:0000). The address returned should be higher than the top location you need, observing that part of the available memory is used for the string and stack blocks, which extend downwards. Those MSX users in possession of a computer with a disk drive may have to limit the DISK BASIC workspace somewhat by holding down the CONTROL key during power-up as a means of telling the system there is but one virtual disc drive available. Similarly holding down the SHIFT key disables the disc unit altogether.

Command summary
Although the proposed program is extremely simple to use, it is none the less recommended to study this brief summary of the available functions, commands and options. After typing CALL EPROMx, you should see the welcome screen. Pass on to the help pages with EPROM data and program information by pressing any key. You can leaf through the help pages by pressing the appropriate cursor movement keys. The command input screen can be called up at any time by pressing the space bar.

The following keys are used during the command input mode:

- Cursor ↑ and ↓ select the item you wish to work on.
- Key H returns you to the help pages.
- Key P causes the screen contents to be dumped to a printer (make sure this is properly connected else you will get a NO PRINTER error).
- Key T runs a test program that causes all functions on the programmer to be successively enabled with aid of CTC interrupts, indicated by the flashing PGM LED. Make sure that jumper J1 is not installed, and never run the test with an EPROM inserted in the ZIF socket.
- The space bar selects the various options for the command items (toggle function).
- Key S causes the program to start executing your set of commands. Always make sure that the command screen shows what you want before pressing S.
- Key I enables the storing of BASIC programs in EPROM.
- The software automatically arranges for the correct initialisation of the memory begin & end, and EPROM begin & end addresses. Link addresses are automatically adapted to enable the BASIC program to be run from EPROM.
- With reference to Fig. 9, these are the various parameters you need to define before the programmer does what you want it to do:

<table>
<thead>
<tr>
<th>EPROM READ</th>
<th>VERIFY</th>
<th>WRITE</th>
</tr>
</thead>
<tbody>
<tr>
<td>2716</td>
<td>0F</td>
<td>06 + Vpp</td>
</tr>
<tr>
<td>2732</td>
<td>06</td>
<td>08 + Vpp</td>
</tr>
<tr>
<td>2764</td>
<td>08</td>
<td>0C + Vpp</td>
</tr>
<tr>
<td>27128</td>
<td>48</td>
<td>0D + Vpp</td>
</tr>
<tr>
<td>2756</td>
<td>1F</td>
<td>0B + Vpp</td>
</tr>
<tr>
<td>27512</td>
<td>0B</td>
<td>0A + Vpp</td>
</tr>
<tr>
<td>256</td>
<td>08</td>
<td>0B + Vpp</td>
</tr>
<tr>
<td>2522</td>
<td>68</td>
<td>0B + Vpp</td>
</tr>
<tr>
<td>2564</td>
<td>68</td>
<td>0B + Vpp</td>
</tr>
</tbody>
</table>

The space bar selects the various options for the command items (toggle function).
- Key S causes the program to start executing your set of commands. Always make sure that the command screen shows what you want before pressing S.
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- The software automatically arranges for the correct initialisation of the memory begin & end, and EPROM begin & end addresses. Link addresses are automatically adapted to enable the BASIC program to be run from EPROM.
- With reference to Fig. 9, these are the various parameters you need to define before the programmer does what you want it to do:

EPROM TYPE and PROGRAMMING VOLTAGE consult Table 1 or the relevant help page and use the space bar to select the appropriate EPROM type; notice that EPROM BEGIN & END change in accordance with the holding capacity of the relevant EPROM type. It is possible to program part of an EPROM by keying in the relevant hexadecimal address range. The program accepts entries in hexadecimal only, and produces an error message if you try to define an impossible address range, or if the EPROM BEGIN & END entry is not in accordance with the MEMORY BEGIN & END entry. Example: you want to program the first half of a Type 2764 (8 Kbyte): EPROM BEGIN = 0000, EPROM END = 01FF, MEMORY BEGIN = 0000, MEMORY END = 01FF.

BLANK CHECK should be a fairly well-known facility; it checks with the aid of EPROM BEGIN & END, whether the specified address range contains only bytes FF, indicating that data can be loaded there.

PROGRAM speaks for itself. This function uses both EPROM BEGIN & END and MEMORY BEGIN & END and verifies that the EPROM contents and the RAM buffer contents are the same, and evidently uses EPROM BEGIN & END and MEMORY BEGIN & END to determine what address ranges are to be compared.

READ AND RUN CHECKSUM reads the data from the EPROM into the buffer and adds the values of all bytes to produce a 16 bit checksum.

DISPLAY MEMORY offers the user the possibility to load the EPROM contents into the computer for examination on the screen (hexadecimal and ASCII format, 8 bytes per line, preceding address). You can not alter the displayed bytes.

PROGRAM MODE simply selects normal, fast-1, fast-2, or intelligent programming as appropriate for the specific type of EPROM. Consult Table 1 or the relevant help page.

ADDRESS COUNTER at the lower end of the screen is a 16-bit counter that keeps track of the EPROM location currently read from or written to.

The RESULT line at the bottom of the screen can be used to display the following messages (H returns to the help pages): ADDRESS ERROR is a general message to tell you to re-do the EPROM BEGIN & END and/or the MEMORY BEGIN & END entry before pressing S again BLANK reports that the stated address area contains only bytes reading FF. The EPROM area is not copied into RAM.

NOT BLANK reports that one or more bytes in the specified EPROM area do not read FF.

The address counter displays the first address encountered, and the program is halted.

READING COMPLETED speaks for itself. The contents of the EPROM are available for examination with DISPLAY MEMORY. For modification, you will probably want to resort to BASIC or a suitable utility package.

VERIFY reports that the verification routine has completed without finding errors.

VERIFY ERROR indicates that one or more differences exist between the contents of the EPROM and that of the RAM.

The address counter displays the first incorrect address encountered, and the program is halted.

REPROGRAMMABLE indicates that a verify error was found, but the relevant byte is reprogrammable, i.e. any of its bits reads logic 1 when it should be logic 0 or logic low levels in EPROMs can only be changed into logic high by exposing the chip to a dosage of ultra-violet light.

NOT REPROGRAMMABLE re-
ports that the address indicated by the address counter can not be loaded correctly even after applying 25 programming pulses (see Table 4, intelligent programming only).

EXECUTION STOPPED is displayed in response to the pressing of the RESET switch on the EPROM programmer.

DEVICE I/O ERROR indicates that the computer is not receiving interrupts from the cartridge, which is possibly set to the wrong I/O address.

NO PRINTER is a message that speaks for itself.

ILLEGAL COMMAND ORDER informs you to re-do the YES/NO setting of one or more commands. Note that it is allowed to choose YES for BLANK CHECK, PROGRAM and VERIFY, the program performs these steps in the correct order, without the need for intermediate command starting with S.

As already noted, it is advisable to thank well before pressing the 5 key and to start the program. If you get an error report, do not get into a panic, just study the command screen to trace the fault and understand its nature. Once you have worked with this EPROM programmer for some time, you will notice that it is highly user-friendly and easy to get going with the aid of the help pages, which are instantly available at the pressing of key H.

If you do not know how to program an EPROM which is not included in Table 1, simply begin with the lowest programming voltage, 12.5 V, to see if anything happens to the contents of the device, you cannot damage it in this way, provided you do not select intelligent programming, as this causes the Vcc line to be raised to 6 V during the programming cycle in conclusion of this section, a few more tips. When an EPROM is stated to be programmable in the normal (50 ms) mode, it is worth while to try out the effect of selecting fast 1 or fast 6 programming to save time. If you want to document the program settings for a specific EPROM, it is a good idea to use the screen dump option for the recording of the checksum and other relevant data. Remember that a Type 27512 (54 Kbyte) EPROM must be programmed in two 32 Kbyte passes. Press CONTROL-STOP to return to MSX BASIC, and type CALL EPROMX to run the programmer again. Use an assembler or a machine language utility package to write bytes into the RAM buffer for loading into an EPROM, but make sure that data is not overwritten by stack or buffer usage of any program you run in combination with the EPROM programmer software.

Keep in mind that running BASIC programs that use PLAY commands require the computer to be reset and hence the EPROM programmer software to be re-initialized. This is because the proposed program replaces its jump table and variable map in the voice queue area. In more general terms, do not use the EPROM programmer software before you are sure that there are no other programs, or remnants thereof, still around somewhere in the computer's memory. The best way to avoid trouble is to reset the machine with the EPROM cartridge inserted.

Finally, Table 5 shows the control words for the various EPROM types. These 7-bit words are specific to the EPROM type to be dealt with, and can be used by anyone contemplating the writing of his own version of the control software.

Getting started

Commence with fitting jumpers B D E and I on the EPROM cartridge board, then mount EPROM SS 555 in the 28-way socket. Plug this cartridge into a slot of the MSX computer, and plug the I/O & timer cartridge either in a remaining slot, or in the one provided on the EPROM cartridge board. Connect the EPROM programmer to the I/O & timer cartridge via the 90-way flat ribbon cable, and have the system ready for use. See Fig. 10. Please note that it is not possible to use the add-on busboard for MSX computers, in conjunction with the timer & I/O cartridge. Do not yet fit an EPROM in the ZIF socket, switch on the power, and call the program on completion of its installation. After viewing the welcome and copyright screen, go to the command screen and run the built-in test routine prior to working on any EPROM. If all LEDs on the programmer's front panel can be seen to go on and off at regular intervals, there is good reason to assume that the hardware and software functions satisfactorily, and it is high time to set the system to work on any EPROM that you may have available.

AR

We regret that we can not provide information on the use of this EPROM programmer with computers other than those in the MSX series.

Previous articles on MSX extensions have appeared in the following issues of Elektor India:

February 1986 (I/O bus, digitizer, I/O port);
March 1986 (EPROM cartridge board);
April 1986 (add-on bus board);
February 1987 (I/O and timer cartridge).

Fig. 10. One slot can hold both the EPROM and the I/O cartridge.
Local Area Networking

The proliferation of personal computers (PCs) as a business tool has driven the need for a distributed processing environment where many microcomputers can share expensive peripheral devices, such as printers and hard disk drives. The capability to network equipment also enables users to share files and programs and to centralize backup facilities and procedures.

Local Area Networking has two main requirements. It must be implemented in VLSI, to simplify design and lower the overall "per node" cost of connection to a network. Second, the LAN must also run standardized software and conform to an industry standard, so that end users can interconnect equipment from different vendors without worrying about protocols.

The IEEE 802.3 standard (Ethernet™) has gained wide acceptance by both large and small companies as a high-speed (10 megabit/second) LAN. However, because of its cable requirements, it can be relatively expensive to implement. In response to this drawback, Thin Ethernet, also known as CheaperNet™—was developed. Thin Ethernet uses less expensive coaxial cable and features a "node-integrated" transceiver. Thin Ethernet maintains full compatibility with Ethernet's 10 megabit/second data rate.

Another network sponsored by the IEEE 802.3 committee is StarLAN™, a 1 megabit/second implementation that features a "star" configuration. Each node is connected to another central hub in a point-to-point fashion.

Continuing development of LAN interface chips has driven the LAN connection cost per node down to new levels, making networks affordable at all business levels. Because of its cost-effectiveness, the personal computer connection segment of the LAN marketplace is forecast to grow faster than any other segment. According to Dataquest, revenues in 1989 will top US$ 528 million. Revenues in 1986 totalled US$ 181.7 million. The installed base of networked PCs will be 3.7 million in 1990, up from 438,000 in 1985.

Current Status

The decision by 3Com and Novell to port their LAN operating system to National Semiconductor's DP3890 Network Interface Controller marks the first time a semiconductor supplier has taken an active role in making network software standard with their chips. This makes it easier for designers to use the chips in a network, rather than having to write software themselves. For original equipment manufacturers (OEMs), DP3890 compatibility with 3Com's 3+ network software and Novell's Advanced NetWare means an easy path to LAN design for IBM-compatible PCs. OEMs can use National Semiconductor's tool kit containing DP389EB LAN evaluation boards and 3Com 3+ network software to develop networking and workgroup computing products. Or they can use Novell's development kit and the DP389EER or the DP3890 LAN chip set to design local area networks.

3Com and Novell are responsible for setting "de facto" standards in the PC LAN industry. 3Com is the leading vendor of LAN add-on boards for PCs, with a 19 percent share of the market, according to Dataquest, Novell's NetWare, with 60,000 installations, is the most widely used PC LAN operating system. It supports 35 local area network systems, including 3Com's Etherlink and Etherlink Plus. AT&T's StarLAN and IBM's PC Cluster and Token Ring Network.

Support from two predominant LAN suppliers reflects the emergence of the DP3890 as the standard LAN chip set of choice among system designers.

National's Local Area Network Chip Set

Focusing specifically on the IEEE 802.3 local area network standard encompassing Ethernet, Thin Ethernet (CheaperNet), and StarLAN compatible networks, National designers developed three integrated circuits: an Advanced Network Interface Controller (DP3890 NIC), a Serial Network Interface (DP3891 SNI), and a Coaxial Transceiver Interface (DP3892 CFI). The chip set was the first complete VLSI implementation to meet the entire IEEE 802.3 standard. Its availability makes National Semiconductor well positioned to provide the rapidly expanding PC LAN market with its cost-effective chip set. In particular, the DP3895 was the first monolithic chip implementation of a cable transceiver. The high level of integration saves users a significant amount of board space. In fact, the network chip set is the only one that fits on a short-slit PC card.

The DP3890 NIC features two 16-bit DMA channels that deliver all the data-link layer functions required for data packet transmission and reception. The DP3891 features a patented digital phase lock loop for most reliable data reception. The DP3892 CFI implements all driver receiver, jabber and collision-detecting functions required by the IEEE 802.3 cable transceiver. In addition, the DP3890 exceeds the one million hour MTBF required in the 802.3 specification for transceivers.

Illustrating National Semiconductor's technological breadth, three distinct process technologies were used in fabricating the chips: microCMOS for the DP3890, a high-speed oxide isolated bipolar process for the DP3891, and a junction-isolated bipolar process for the DP3892. The DP389EB evaluation board, containing the chip set, plugs into any IBM PC-compatible computer and incorporates all of the components required to provide a LAN interface to Ethernet or Thin Ethernet networks.

The entire LAN chip set and evaluation board are all currently in production.

Ethernet is a trademark of Xerox Corporation.

CheaperNet is a trademark of National Semiconductor Corporation.

StarLAN is a trademark of AT&T Bell Laboratories.

(Source: National Semiconductor)
From magnifying glasses to microscopes, callipers to computer interfaces, pipettes to pH meters: the range of equipment in the catalogues of educational laboratory suppliers is vast and continually changing in response to technological and educational developments.

The introduction of microcomputers such as the BBC B and the RM 3802 to schools in the United Kingdom has been rapidly followed by the development of computer interfaces whose purpose is either to enable data from school laboratory experiments to be captured, processed and displayed, or to control simple devices. Similarly, the rapid adoption by industry of biotechnological techniques has led Britain's major suppliers to sell kits by which these techniques can be simulated in schools. However, not all new equipment is simulated by recent technological innovations: an educational concern to introduce science and technology to pupils aged five to 11 has led to much recent interest in construction kits. There are several reasons why the range of educational laboratory equipment is so wide. First, science is taught in British schools over the age range of five to 18. Secondly, as the emphasis is on giving pupils hands-on experience, suppliers have learned how to provide equipment that schools can afford in quantities sufficient for classroom work. Some of it is, of course, for demonstration by teachers but much is meant to be used by pupils working in groups of two and three. Pupil practical work is a cornerstone of education and so equipment must be strong and relatively inexpensive.

**Higher education equivalent**

The high degree of specialization by British pupils who stay on after 16, in many cases to prepare for a course at a university or polytechnic, is sometimes criticized but it has advantages. The level reached by these pupils is typical of first or even second year university students in some countries and so equipment that is useful for higher education is produced in the quantities that schools need and at prices they can afford. Britain does not have a centralized educational system and schools are given extensive choices in the courses they provide for their pupils. There are eight area boards offering examinations for the more academic pupils, and while procedures ensure comparability between these examinations, syllabuses do vary which increases the range of equipment needed.

In the 1960s and 1970s, there was considerable interest in British schools, much supported by money from the Nuffield Foundation or from the government-funded Schools Council.
This stimulated corresponding innovations in equipment, another reason why the range is so wide.

In the late 1970s and early 1980s, most of the new courses were revised and these revisions, together with restrictions on money for education caused by failing school rolls and the worldwide slump, have eliminated from the suppliers' catalogues anything that has proved unpopular. Much of what is left — still very extensive — has been refined over at least a decade.

British schools tend to buy their laboratory equipment from three main general suppliers: Griffin & George (GG), Philip Harris (PH) and Irwin-Desman (ID). However, a number of specialist firms are also used. For example, interfaces for coupling equipment with microcomputers to capture data or demonstrate control are offered by all three of the major suppliers as well as the specialists.

**Many functions**

An example is the measurement module supplied by Educational Electronics (EE) which enables data from the outputs of a range of instruments — Hall probes for measuring magnetic fields, pH meter and so on — to be recorded and strikingly displayed in several forms on a television monitor. A range of sensors is being developed to go with this and other computer interfaces. One of the most interesting recent developments using micro-electronics is the GiPSI (Griffin Programmable Scientific Instrument). There is concern that much of the more sophisticated equipment used in education spends much of its time on the shelf and is used only when its turn comes round in the syllabus, so this instrument has many functions and will measure current, voltage, resistance, magnetic field, pH, light levels, and so on. The function wanted is selected by connecting a module containing an appropriately programmed read only memory (ROM) and lifting overlays over the control panel makes it easy to use.

Another current growth area is electronics teaching kits. There have for many years been small components of electronics in some school physics courses but such physics teaching has recently been modernized and separate school electronics courses developed. The emphasis has shifted from simple introductions to semiconductor diodes and triodes to a systems approach to digital electronics and to operational amplifiers.

There are currently many approaches to teaching electronics embodied in kits. The equipment for one very popular course, "Micro-electronics for All", intended for 11 to 13 year olds but in fact used for older pupils as well, is available from Unilab (U) ideas underlying micro-electronics — or information technology as it is sometimes called — are learned through solving simple control problems. Other kits drawing interest are the Independent Schools Micro-electronics Centre (ISMEC) kits available from Griffin & George, Philip Harris, and Unilab. Unilab specialises in electrical and electronic equipment for education at competitive prices such as power supplies, meters, radiation counters, signal generators, and so on, all items that can, of course, be obtained from the general suppliers.

**Move to plastics**

It is easy to look just at recent major developments and forget that the bulk of purchases made by educational establishments is for consumables, notably glassware and chemicals, both supplied by Griffin & George and Philip Harris. Another company that specializes is BDH Chemicals. A development over the last few years has been the slow acceptance by schools of plasticsware in place of glassware. Early examples of plasticsware stayed too readily but recent products are more satisfactory and stand up to pupil use much longer than glass. Many of the top pan balances bought during the boom in science education in the 1960s and 1970s are now wearing out and schools are replacing them, as lunds permit, with electronic balances with digital displays. These are very quick to use so that fewer are required for a class. Griffin & George, Philip Harris, and Irwin-Desman all supply balances but there are also several specialist suppliers, notably Oertling.

There are several ranges of microscope and specialist firms such as Prior have suitable instruments for the educational market. Recently, biologists have shown interest in kits for environmental studies containing meters that measure pH, conductivity, temperature, light level, and so on. An example is an enzyme kit which provides insight into the industrial use of biotechnology. A recent growth point has been equipment for primary school science. The educational emphasis is on using what can be found in the home and the classroom with the minimum use of special equipment but some is needed, such as simple kitchen-type scales, magnifiers, thermometers, construction kits, and so on. Specialist primary school companies such as E.J. Arnold and Osborn have equipment suitable for primary science education.

**Checking for safety**

The School Science Service provides information and consultancy on school science equipment and safety for the majority of British schools. Its task is to examine and test equipment and make recommendations to teachers. Copies of its reports can be obtained overseas through the British Council or through subscribing to the service as an overseas associate. Frequently the service is obliged to be critical of certain products but suppliers usually make modifications in the light of criticisms.

E.J. Arnold Ltd, Lockwood Distribution Centre, Parkside Lane, Leeds, West Yorkshire, England, LS11 5TD.

BDH Chemicals Ltd, Broom Road, Parkstone, Poole, Dorset, England, BH12 4NN.

Educational Electronics, 28 Lake Street, Leighton Buzzard, Bedfordshire, England, LU7 8RZ.

Griffin & George Ltd, Bishops Meadow Road, Loughborough, Leicestershire, England, LE11 3RG.

Philip Harris Ltd, Lynn Lane, Shenston, Staffordshire, England, WS14 0EE.

Irwin-Desman Ltd., 294 Purley Way, Croydon, Surrey, England, CR9 4GJ.

Oertling, W. & T Avery Ltd, Smeethwick, Wakefield, West Midlands, England, B95 2LP.


Prior Scientific Instruments Ltd., London Road, Bishops Stortford, Hertfordshire, England, CM23 5ND.

Unilab Ltd, Clarendon Road, Blackburn, Lancashire, England, BB1 9TA.
Hot ICs - no need for fear

It is perfectly normal for ICs, particularly bipolar digital ICs such as TTL, to become very warm in operation. These ICs draw considerable power which is finally dissipated as heat. An example is the common TTL IC 74145. Typical dissipation for this device is 215 mW and approximately 360mW maximum, this is in the quiescent state with unloaded outputs. When these are loaded the dissipation is even higher. Since the area of the IC package is relatively small, the IC becomes very warm indeed. This is no problem, however, it is rated appropriately and operates perfectly even at ambient temperatures of up to 70°C. When the computer is installed in a housing, care should be taken to provide ventilation slots for the heat to dissipate. In the event of doubt regarding the temperature rise of ICs, the data sheet should be consulted, an IC with a maximum dissipation of 10 mW for instance, should not exhibit noticeable temperature rise.

The Microcomputer as a source of interference

Every microcomputer system operates with relatively fast logic ICs, such as Schottky TTLs. This means that the digital signals have fast-rise slopes which produce harmonics extending far into the VHF/UHF region. This causes interference, and not only to FM stereo reception. The problem is not restricted to home made microcomputers, some commercially built microcomputers, particularly teaching and experimental systems, can unfortunately be classified as sources of electromagnetic pollution. The only solution is to install the microcomputer in a metal screened housing, with an earth connection, it may also be necessary to fit a mains RF suppression filter. Screened (coaxial) cable should be used for connections between the computer and peripheral equipment. These precautions apply to all digital equipment using fast logic.

This particular topic receives full attention in Junior Computer Book 2 (to be available shortly), but there is no harm in whetting the appetites of our readers even if it is a little premature.

How can the Junior Computer display words? Normally speaking, data and address information is displayed with the aid of the monitor routine SCANDS. This involves one of the hexadecimal numbers, 0 . . . F, in each display. Where texts are concerned, however, the monitor routines are no good. What is needed is the subroutine SHOW with the addition of a special look-up table which contains the corresponding seven segment pattern for each individual letter.

Table 1 provides a survey of letters and figures together with the corresponding data which has to be entered into port A for them to be displayed. This table has been partly based on suggestions made to us from one of our readers. Obviously, letters which include diagonal lines (such as K, M, N, Q, V, W, X and Y) will have to be adapted to the horizontal and vertical set up of the display segments. Experience has shown, however, that the eye and the brain soon become accustomed to this.

Now for a short program that will allow a six letter word to appear on permanent display. A good example would be the word 'Junior' as indicated on the prototype of the Junior Computer in the front cover photograph of the May 1980 issue of Elektor and Book 1. The program, JUNIOR, is listed in table 2.

Here the modified SHOW routine will be called SHOWDS and the look-up table that holds the information relating to the display of any particular character is called TXT (text table). The Y index register acts as the display counter and text index. The value contained in the Y register increases from 00 to 05 as an index for the particular character to be displayed. As soon as the value in the Y register becomes 06,
Table 1.

| 0 | 40 | E | 06 | a | 23 |
| 1 | 79 | e | 04 | P | 6C |
| 2 | 24 | F | 0E | q | 10 |
| 3 | 30 | G | 42 | R | 2F |
| 4 | 19 | g | 09 | I | 10 |
| 5 | 12 | H | 09 | S | 12 |
| 6 | 92 | h | 09 | I | 87 |
| 7 | 78 | i | 7A | U | 63 |
| 8 | 60 | i | 6F | V | 41 |
| 9 | 10 | J | 72 | W | 01 |
| A | 08 | K | 0A | X | 36 |
| b | 20 | L | 47 | Y | 11 |
| c | 03 | l | 4F | Z | 64 |
| d | 46 | M | 4B | – | 3F |
| e | 27 | n | 2B | – | 37 |
| f | 21 | O | 1B | 4F | 7F |

Table 2.

| JUNIOR | 0209 |
| JUNIOR | 0202 |
| DISMEX | 0205 |
| DISMEX | 0207 |
| DNE DIS | 0209 |
| DNE DIS | 0208 |
| SHOWIDS | 0208 |
| SHOWIDS | 0207 |
| SHOWIDS | 0216 |
| SHOWIDS | 0210 |
| SHOWIDS | 0212 |
| SHOWIDS | 0214 |
| SHOWIDS | 0216 |
| SHOWIDS | 0218 |
| DELAY | 0222 |
| DELAY | 0223 |
| DELAY | 0225 |
| DELAY | 0226 |
| DELAY | 0227 |
| DELAY | 0228 |
| DELAY | 0230 |
| DELAY | 0232 |
| DELAY | 0234 |
| DELAY | 0236 |
| TXT | 0238 |
| TXT | 0239 |
| TXT | 0241 |
| TXT | 0242 |
| TXT | 0244 |
| TXT | 0246 |
| TXT | 0248 |
| TXT | 0250 |
| TXT | 0252 |
| TXT | 0254 |
| TXT | 0256 |

The function of the X index register, on the other hand, is the same as it was for the SHOW routine: it acts as a display digit switch by way of port B. In other words, the information contained in the X register (08, 0A, 0C, 0E, 10 and 12 consecutively) is passed to port B data register to turn each of the displays on in turn.

Text on the run... A stationary text is all very well, but it does tend to get a little monotonous after a while. A much more interesting possibility would be to update the displayed text every few moments. In this manner whole sentences could be displayed instead of just single words. This can be accomplished with the aid of the program JUNTX shown in table 3. The effect is very similar to that of an electronic news display. It is an expanded version of the earlier program JUNIOR (table 2). Page 03 is used to store the actual text which can, therefore, be up to 256 characters in length.
Table 3.

<table>
<thead>
<tr>
<th>JUNTXT</th>
<th>0200</th>
<th>A9 7F</th>
<th>LDA = 7F</th>
</tr>
</thead>
<tbody>
<tr>
<td>0202</td>
<td>BD 81 1A</td>
<td>STA-PADD</td>
<td></td>
</tr>
<tr>
<td>0205</td>
<td>A5 00</td>
<td>LDAZ-NUVAR</td>
<td></td>
</tr>
<tr>
<td>0207</td>
<td>38</td>
<td>SEC</td>
<td></td>
</tr>
<tr>
<td>0208</td>
<td>E9 05</td>
<td>SBC = 05</td>
<td></td>
</tr>
<tr>
<td>020A</td>
<td>BS 02</td>
<td>STAZ-NUMCOR</td>
<td></td>
</tr>
<tr>
<td>BEGIN</td>
<td>022C</td>
<td>A9 00</td>
<td>LDA = 00</td>
</tr>
<tr>
<td>DSTIME</td>
<td>022E</td>
<td>BS 01</td>
<td>STAZ-NUMCOR</td>
</tr>
<tr>
<td>DISMPX</td>
<td>0212</td>
<td>A9 0F</td>
<td>LDA = 6F</td>
</tr>
<tr>
<td>ONEDIS</td>
<td>0218</td>
<td>A9 0F</td>
<td>STAZ-DICNT</td>
</tr>
<tr>
<td>021A</td>
<td>18</td>
<td>DX = 08</td>
<td></td>
</tr>
<tr>
<td>0218</td>
<td>BS 04</td>
<td>STY2-TEMPY</td>
<td></td>
</tr>
<tr>
<td>021C</td>
<td>65 01</td>
<td>TYA</td>
<td></td>
</tr>
<tr>
<td>021E</td>
<td>A8</td>
<td>CLC</td>
<td></td>
</tr>
<tr>
<td>021F</td>
<td>28 39 02</td>
<td>ADCZ-NUMCOR</td>
<td></td>
</tr>
<tr>
<td>0222</td>
<td>A4 04</td>
<td>TAY</td>
<td></td>
</tr>
<tr>
<td>0224</td>
<td>CS</td>
<td>JSR-SHOWDS</td>
<td></td>
</tr>
<tr>
<td>0225</td>
<td>C9 06</td>
<td>LDV2-TEMPY</td>
<td></td>
</tr>
<tr>
<td>0227</td>
<td>FA 02</td>
<td>INY</td>
<td></td>
</tr>
<tr>
<td>0229</td>
<td>D9 ED</td>
<td>CPY = 06</td>
<td></td>
</tr>
<tr>
<td>TMCHK</td>
<td>022A</td>
<td>BS 03</td>
<td>BED-TMECHK</td>
</tr>
<tr>
<td>022B</td>
<td>CS 06</td>
<td>BNE-ONEDIS</td>
<td></td>
</tr>
<tr>
<td>022C</td>
<td>D8 05</td>
<td>DEQDZ-DICNT</td>
<td></td>
</tr>
<tr>
<td>022D</td>
<td>BE 05</td>
<td>INCZ-NUMCOR</td>
<td></td>
</tr>
<tr>
<td>0231</td>
<td>A5 02</td>
<td>LDAZ-NUMCOR</td>
<td></td>
</tr>
<tr>
<td>0233</td>
<td>C5 01</td>
<td>CMPZ-NUMCOR</td>
<td></td>
</tr>
<tr>
<td>0235</td>
<td>BD 09</td>
<td>BCS-DSTIME</td>
<td></td>
</tr>
<tr>
<td>0237</td>
<td>90 03</td>
<td>BCC-BEGIN</td>
<td></td>
</tr>
<tr>
<td>SHOWDS</td>
<td>0239</td>
<td>BS 09 03</td>
<td>LDA-TXT, Y</td>
</tr>
<tr>
<td>023C</td>
<td>BD 88 1A</td>
<td>STAPADD</td>
<td></td>
</tr>
<tr>
<td>023F</td>
<td>BE 82 1A</td>
<td>STX-PBD</td>
<td></td>
</tr>
<tr>
<td>0242</td>
<td>A0 7F</td>
<td>LDY = 7F</td>
<td></td>
</tr>
<tr>
<td>DELAY</td>
<td>0244</td>
<td>BB</td>
<td>DEY</td>
</tr>
<tr>
<td>0245</td>
<td>10 FD</td>
<td>BFL-Delay</td>
<td></td>
</tr>
<tr>
<td>0247</td>
<td>BS 88 1A</td>
<td>STY-PAD</td>
<td></td>
</tr>
<tr>
<td>024A</td>
<td>A9 05</td>
<td>STY-PBD</td>
<td></td>
</tr>
<tr>
<td>024C</td>
<td>BS 82 1A</td>
<td>INX</td>
<td></td>
</tr>
<tr>
<td>024F</td>
<td>08</td>
<td>INX</td>
<td></td>
</tr>
<tr>
<td>0250</td>
<td>E8</td>
<td>RTS</td>
<td></td>
</tr>
<tr>
<td>0251</td>
<td>50</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

PA8 .. PA6 are outputs
contents NUM (0000) to accumulator
C = 1
NUMCOR = NUM minus 05
first display text
establish text display time
start from D1
display counter (Y) = 00
store display counter
Y to accumulator
C = 0
A + Y + contents NUMVAR (0001) accumulator to Y
display first/next character
invalidate state of display counter
increment display counter
all have 6 display been accessed?
if yes, move on to time check
if not, next display
line up?
if not, repeat present text
if yes, update text
and of text?
if not, show new text
if yes, start again

— enough for the average length paragraph!
Again, this program uses the subroutine SHOWDIS, only this time the text table (TXT) is located at address 0220 and although the Y register is still used as a display counter it is no longer used as a text index directly, instead, the particular section of the text to be displayed is calculated by adding the instantaneous value in the Y register to the contents of address location NUMVAR (0001).
The value contained in NUMVAR will be constant for the period of time a certain text is on display (the actual duration can be adjusted by modifying the contents of location 0211). As soon as that period of time is over the contents of NUMVAR are incremented by one: the entire text shifts one location to the left and the right hand display shows a new character. When the contents of NUMVAR are greater than the contents of location NUMCOR, we will have arrived back at the beginning, as this means that the entire text will have been displayed. This is because the contents of NUMCOR are 05 less than those of location NUM. The latter (location 0000) is where the user must store the low order byte of the last memory location of the text table. In other words, if the last character of the text message is stored in location 0332, the value 32 is stored in location 0000 (NUM).

Table 4 provides a sample text which can be displayed on the Junior Computer with the aid of the program JUNTXT as given in table 3. The text contains a message for Junior Computer Book 1 owners. A text should always be preceded by at least six blank spaces (7F), so that the beginning and end of the message are clearly separated from each other.

<table>
<thead>
<tr>
<th>Location</th>
<th>Text</th>
<th>Hexadecimal</th>
</tr>
</thead>
<tbody>
<tr>
<td>0300</td>
<td>01</td>
<td>7F 7F 7F 7F 7F 7F 7F 7F</td>
</tr>
<tr>
<td>0308</td>
<td>02</td>
<td>00 23 07 7F 7F 7F 7F</td>
</tr>
<tr>
<td>030F</td>
<td>03</td>
<td>00 23 07 7F 7F 7F 7F</td>
</tr>
<tr>
<td>0310</td>
<td>04</td>
<td>00 23 07 7F 7F 7F 7F</td>
</tr>
<tr>
<td>0312</td>
<td>05</td>
<td>00 23 07 7F 7F 7F 7F</td>
</tr>
<tr>
<td>0314</td>
<td>06</td>
<td>00 23 07 7F 7F 7F 7F</td>
</tr>
</tbody>
</table>

0000 (NUM) = 34
It is a well known fact that the sound of the roaring surf of the oceans is the most satisfying sound in our environment. Those who have experienced the magic of this sound in an otherwise calm surroundings will immediately agree. It is quite a fantastic feeling to sit on the beach, close the eyes and listen to the sound of the seal. The body and nerves which have been subjected to tremendous stress of the day to day life get relaxation from this sound and derive renewed force and energy.

Unfortunately most of us can enjoy this pacifying experience once in a while, during holidays. For those who are deprived of this luxury, we present here a small circuit which can generate the 'Sound of the Sea'. The circuit can be built from just a few components and imitates the sound of the sea in an excellent manner. This can also be used as background for a session of viewing your slides of a holiday on the beach.

**The Circuit**

As it is already indicated, the circuit must produce the sound of the sea. This is done by the part A of the circuit block diagram shown in figure 1. In order to imitate the rising and falling of the roaring surf, it must have a control for the sound. This control is provided by the blocks B and C. Block B is an astable multivibrator which produces a rectangular pulse train, with a non symmetrical duty cycle. From the pulse train, block C generates a saw tooth waveform with a rapid rise and slow fall. Both these signals are fed to the input of an amplifier block D in such a way that the signal from A is amplified by block D with amplification proportional to the signal coming out of block C. The rising and falling of the sound is created by this sawtooth waveform.

Let us now see the practical
circuit. Figure 2 shows the noise source. This is an unusual connection for a transistor. The NPN transistor T1 is connected in a reverse manner using only the base emitter junction. The collector is left unconnected. A transistor connected in such a manner behaves in a very noisy way. The intrinsic noise of a semiconductor device is a complex phenomena and will not be discussed here. The reverse biased base emitter diode behaves somewhat like a zener diode. A reverse current flows through this diode and resistance R1. The noise component in this current is connected to the next stage via capacitor C1.

Figure 3 shows the next stage which is an amplifier controlled by an astable multivibrator. T2 is the amplifier of block D. T3 and T4 form the astable multivibrator. The potential divider R2 and R1 give the proper bias voltage to the base of T2 through R3. The setting of potentiometer P1 decides the minimum volume of the sound of the sea.
In absence of the signal from the astable multivibrator, the sound would be a continuous noise tone. To convert it into a rising and falling roar of the surf, the control signal is fed to the base through R6. The AMV (astable multi-vibrator) formed by T3 and T4 produces a rectangular wave as shown in figure 4. The frequency of this control signal is about 1/8 Hz. This low frequency is required for the most realistic effect. The C4 (R7+P2) combination produces a sawtooth wave from the rectangular signal. The sawtooth wave is a result of charging and discharging of capacitor C4. During the OFF period of T3, the capacitor C4 charges through R8 and D1. During the ON period of T3, the charging can no more continue, but discharging can take place through R7 and P2. The values are so selected that by the time C4 is discharged, the next charging cycle starts again. As R7 and P2 form a potential divider, the signal fed to base of T2 depends on setting of P2. In technical language, the C4 and (R7+P2) combination is said to be an integrator which integrates the signal at the collector of T3. The sawtooth signal is superimposed on the constant DC level set by P1 at the base of T2 and the resulting voltage looks like the waveform shown in figure 5 (bottom part). This voltage at the base of T2 controls the amplification factor for the noise signal being amplified by T2. Thus, the output of amplifier T2 rises sharply and falls slowly, similar to the real roar of the surf.

The Construction:
The complete circuit can be accommodated on one small SELEX PCB. Component layout is shown in figure 6. As the circuit layout is a bit crowded compared to other simple SELEX circuits, the placement and soldering should be done carefully. The soldering sequence is as usual — jumper wires, resistors, diodes, capacitors, trimpots, transistors, and finally the soldering pins or lugs for the external connections. Pay proper attention to the polarity of electrolytic capacitors.

An important point to note about the transistor T1 is that it’s collector is not connected anywhere. It should not be left floating around on the board but should be cut off near the transistor casing itself. T1 should preferably be BC 107 and may need some trimpots for selecting a ‘good’ noisy one. To select T1 by trimpots, the circuit of figure 2 can be connected to the Tape or Pickup input of the preamplifier of the Hi-Fi system. If this gives a soft noise output through the speakers, the transistor has good noise properties. After selecting T1, the circuit can be assembled and then the output of T2 should be connected to the Hi-Fi system through the output capacitor C3.

A 9 V miniature battery pack is enough to power the circuit, as the current drawn is between 2.5 to 4.3 mA. However, an ON-OFF switch must be provided. A shielded cable must be used for connecting the circuit to the Hi-Fi system, so that the 50 Hz hum is reduced. The shield wire can be connected to the signal ground.

Adjustments:
Two trimpots P1 and P2 are provided for adjustments. The adjustments are interdependent and should be done as follows.

Both the sliding contacts of P1 and P2 should be fully turned towards the earthed terminal initially. Now P1 is slowly rotated till a soft noise is heard. P2 is then adjusted to get the periodic rising and falling of the sound. P1 can be once again adjusted to get the desired volume for the sound of the sea. Now, close the eyes and relax, imagining that you are already on the beach.

Figure 5
Uc4 is the voltage output from capacitor C4 and UBT2 is the effective voltage at the base of T2 produced by superimposing the two input signals.

The sawtooth is superimposed on the constant DC level set by potentiometer P1. The superimposition provided by T2 is proportional to this signal.

Figure 6
Component layout of the circuit

Part List
R1, R2, R8 = 100 KΩ
R3, R9 = 47 KΩ
R4, R6, R11 = 4.7 KΩ
R7, R10 = 220 KΩ
P1 = 10 KΩ
P2 = 100 KΩ
C1, C3 = 1 μF/16 V
C2 = 2.2 μF/16 V
C4, C5 = 22 μF/16 V
C6 = 47 μF/16 V
C7 = 100 μF/16 V
D1 = 1N4148
T1 = BC 107 or BC 547 B
T2, T3, T4 = BC 547 B

Other Parts
1 Standard SELEX PCB
Soldering lugs
Battery connector
3 V miniature battery pack
Hi-Fi Amplifier system
or an Amplifier of high capacity
TOUCH KEYS

Touch keys, sensor switches, touch switches, TAPs Touch Activated Programmer), these are many names for the touch keys. The principle of operation is the same for all Elektor magazine had developed and published the first touch switch project almost fifteen years ago. Since then there have been many variations and developments and the touch keys have replaced the mechanical keys switches in many sophisticated products. Just touch with a finger and without any "click-clack" the switching operation takes place quickly, safely and quietly.

Principle

The sensing surface of the key consists of two conductive surfaces separated by an insulator. The insulator must have an infinitely high resistance or it can even be an airgap. If these two surfaces are now touched simultaneously with the fingertip, the resistance between them drops below 500kΩ. The exact value depends on various factors like the skin resistance of the individual, the size of the touched area, pressure exerted and even the humidity of the skin.

To understand the working principle, you can carry out a small experiment as follows: Connect two coins to a multimeter with the help of crocodile clips. Set the multimeter in Megohms range. Keep the two coins near to each other with a small airgap between them. Now touch both of them together with the fingertip. The meter now reads a value less than approximately 500Ω. The value falls down further if the pressure is increased or if the fingertip is moistened.

This principle is thus very clear: the skin has a finite resistance and this resistance appears across the two sensor surfaces of the touch key when it is touched with a finger. When the key is not touched, the resistance between the two sensor surfaces is very high. This means that a current can flow between the two surfaces when touched. The two possibilities are shown in Figures 2 and 3.

Figure 2 shows a two-stage sensor. When the key is touched, a current flows into stage A, this activates the output stage C and the relay is energised. But the relay remains energised only as long as the key is touched.

Figure 3 shows a three-stage sensor. When the key is touched, a current flows into stage A, this triggers a flip flop stage B and the flipflop activates output stage C. As the flipflop is latched, the stage C remains activated and relay remains energised even after the finger is removed from the key. To de-energise the relay the key must be touched again, so that the flipflop resets and stage C is deactivated again releasing the relay.

Practical Design

A practical circuit is shown in Figure 4. The functional blocks A, B and C can be easily recognised in the diagram. The first part of the circuit consists of transistors T1, T2 and T3 and the resistors capacitor sensor corresponds to block A. If the sensor is touched, a current flows to the base of transistor T1. Transistors T2 and T3 amplify this current and due to the collector currents of all three transistors passing through R6 and R5 a sufficiently large voltage drop is developed across R6. This brings down the voltage at U1 to almost zero level.

Transistors T2 and T3 are all connected in such a manner that they give maximum possible amplification information about this type of connection (known as Darlington Connection) has already been given in SELEX.

The middle portion of the circuit enclosed with a dotted line in the diagram corresponds to block B which is the flipflop. For proper understanding of the functioning it is assumed that T5 is conducting and T4 is open.

Now when the sensor is touched, U1 drops from 9V to 0V. This is transferred to the base of T5 via D3 (at point U6). The transistor T5 stops conducting and drives T4 into conduction. This condition is retained even after the finger is removed from the sensor.

Next time the sensor is touched, the jump in voltage at U1 from 9V to 0V is connected to the base of T4 via D2 and now T4 stops conducting and drives T5 into conduction again. Figure 5 shows all the voltage at various points U1 to U6 in the circuit. Voltage at U6 is used to activate the last stage C which drives the relay. Stage C consists of transistor T6. Voltage at U6 which is connected to the base of T6 via R12 switches the transistor ON and OFF depending on whether it is 0V or 9V. When U6 = 0V, a current flows through R13 and R12 which develops a positive voltage at the base of T6 and T6 goes into conduction. When U6 = 9V no current can flow through R13 and R12 and T6 is cut off. The relay contacts can be used to switch on any device connected through it.
**Construction**

The complete circuit of figure 4 can be assembled on a double size SELEX PCB (80 x 100 mm). Component layout of the circuit is shown in figure 6. The layout shows two connections in dotted lines between points A—B and C—D, and a connection in solid line between A—D. Connections A—B and C—D are to be used if the complete circuit of figure 4 is assembled. Connection A—D will be used if only the blocks A and C are constructed without using the flipflop circuit of block B. The flipflop will not be required if the switch has to close only for the period when the touch key is touched with a finger.

Figure 7 shows an assembled PCB as per the layout of figure 6. A 9V miniature battery pack is used as the power supply. The current consumption of the circuit is less than 3 mA when relay is not energised. Any 9V battery eliminator can also be used as the power supply, but this needs a change in the value of C1. It should be increased to 10 μF. The relay contact can be

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**Figure 1:** Two coins and a multimeter can demonstrate the principle behind the touch key.

**Figure 2:** Block diagram of the simple version of the touch key. The relay remains energised only as long as the touch key is touched with a finger.

**Figure 3:** Touch key block diagram with a flipflop stage added for latching the relay. The flipflop and the switch toggles every time the key is touched.
connected in parallel with the existing ON/OFF switch of the device that is to be controlled by the touch key; for example a Hi-Fi amplifier system.

The scheme of this connection is shown in figure 8. The connection will be similar for any device. Only precaution to be taken is that the rating of the relay contacts must be suitable for the application. The touch key is universally applicable, if it is properly mounted in a suitable casing and the relay contacts are made available over sockets as shown in figure 9.

**Key Tip**

Construction of key tip can be done according to one's own creativity. The important feature to be remembered is that the two conductive surfaces must be separated by an insulator or air gap. The gap should be so small that it can be easily bridged by the tip of a finger. Two ideas are illustrated in figures 10 and 11. One uses a banana socket with a small lug covered with insulating sleeve inserted from behind.
This type of touch key is very easy to install, as the banana socket comes ready with threading and matching nut. Only thing you have to do is drill a hole on the panel and mount the touch key.

Another type of touch key construction shown in figure 11 uses decorative nails, drawing pins and washers of suitable diameter. This type of construction is very difficult because it requires accurate drilling and soldering. The washer and the nail have to be perfectly concentric and an insulating material must be provided between them if the head of the nail is larger than the internal diameter of the washer. The mounting surface also must be of an insulating material as both the parts are directly mounted on it. Two pins must be accurately soldered onto the washer as shown in figure 11. After inserting these through the panel they can be fixed with adhesive on the back side of the panel. In case the touch key is also assembled directly on the PCB, these pins can be directly soldered on to the PCB instead of fixing with adhesive.
Figure 8.
Touch key connected across the On/Off switch of a Hi-Fi amplifier system.

Figure 9.
The touch key becomes universal, if mounted inside a separate case and relay contacts connected to a socket.

Figure 10.
Touch key constructed using a banana socket and a metal lug with insulating sleeve inserted from behind.

Figure 11.
Touch key using decorative nail/pin and a washer. This requires lots of skill in construction as the drilling and soldering must be very accurate.
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LCR METER
Ande Electric Co. Japan, offers the AG-4311 digital LCR meter. This unit is designed to make measurements as close as possible to actual user conditions for L, C & R components, semiconductors, complex components, electronic materials etc.

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PDWNER FACTOR METER
Riken Instrumentation, Chendighar, have developed a wide range of Power Factor Meters which are claimed to be compact, handy and light weight while they are simple in operation. These meters available in MINOR MAJOR, CLIPON and PANEL types. The instruments conform to TS 1248-88 and are type tested.

SLIDE SWITCH
"TEC" have now introduced new Slide Switch with a rating of 2 Amperes, 250V AC/DC. This Slide Switch is available in single pole, on-off sequence with insulation resistance of 100 M ohms and can withstand high voltages up to 2kV. This switch has a bakelite body with brass terminals and red ABS operating knob. The terminals can be provide with screw contacts or solder contacts. The switch is expected to have a mechanical life of more than 20000 cycles and electrical life over 10000 cycles.

For further information please write to
RIKEN INSTRUMENTATION
18/3 Industrial Area Phase I
Chendighar - 160 002.

MECO DPMs
MECO has just introduced 3 new series of light weight Digital Panel Meters featuring slim profiles with large display. These models are numbered GM-135A/ 8 31/2 digit LED type), GM-035 A/B (31/2 digit LCD type) and GM-045 A/B (41/2 digit LCD type).

These 3 new series feature automatic polarity switching, automatic zero function over range indication and built-in hold function. The LED Modules operates on 0-5V power supply while the LCD Module operates on 0-9V power supply. Decimal point selection, high input impedance (more than 100 M ohms) are additional features.

Specifications common to all models include bias current 1 pA typical and 10pA maximum, measurement precision of 0.1% ± 2 digit for 3 31/2 digit and 0.05% ± 3 digit for 4 1/2 digit instruments; typical temperature coefficient of 1 ppm/C for reference voltage, operating and storage temperature ranges 0-50°C and minus 10°C to 60°C respectively and sampling speed of 2.5 times per second.

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IGE - CNC
IGE (India) Ltd. introduces Computer Numerical Controls in technical collaboration with General Electric, USA. GE has pioneered the introduction of Numerical Controls and CNCs in India, and IGE will manufacture GE's Mark Century 1050 H/L, 2 axis, high-speed and Mark Century One for less complex machine tool applications, including turning, milling and machining centres.

UNITERRUPTIBLE POWER SUPPLY
'PROFILE' has introduced an UPS system which is designed to provide pure power with or without mains, with a reported efficiency of more than 80%.

DIP REED RELAY
PLA series DIP reed relays are now available with 1C/0 and 2C/0 contacts as well, besides 1N/0 and 2N/0 contacts. Suitable for mounting on a standard Dual-In-Line IC socket, PLA series DIP reed relays are available with various coil voltages with contacts capable of switching 10W/VA at 0.5 amps and 100V Max. Salient features include high speed switching and excellent input to output isolation characteristics.

KEYBOARD SWITCH
M/s Darshana Industries has introduced an indigenous low Profile Tactile Keyboard Switch. It is a 12mm x 12mm, Four terminal SPST N/O Switch rated at 50m A 12 VDC, and has a 0.3mm movement. It features a detachable A.B.S. keytop with an acrylic cover. Legend may be hot stamped. Engraved or stuck on the A.B.S. top and the acrylic cover snap fitted on top. The switch is available with silver/gold plated contact terminals.

DASA SYSTEM
A data acquisition and signal analysis system (DASA) developed by Gould Inc., USA, is now available through Larsen & Toubro Limited (L&T). It is designed to record, display and analyse all kinds of test results. It has application in such wide testing areas as shock and vibration, stress and strain, biometrics, acoustics, engine analysis, crash testing, high voltage power line failures, data stream quality and accuracy, biophysical research and many other types of test and analysis based on multiple analog signal inputs.

MINIATURE SWITCH
SWITCHCRAFT now offers a miniature toggle switch type T-202, D.P.D.T. rated for 2A-250V AC. These switches can be used for electrical & electronic applications, in telecommunication, electronic data processing etc. They are made of copper, silver plated and terminals are solder lug type, the switches are tested for electrical life at full capacity load for 25,000 operations. The overall dimensions of the switch behind the panel are 13x12.7x14.8 mm the mounting is on 6 mm dia threaded bush.

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**CORRECTIONS**

**Precision power supply**
February 1987 p. 2.49

C1, C2 is 1000 u/25 V and R22 is 0.22 /3W as shown in the circuit diagram, above values are shown wrongly in the part list.

COMPUTERSCOPE-2
February 1987 p. 2.51

Hard copy of the screen image may be made in one of 3 ways:
1. Write the screen contents into a disk and print it later.
2. Use a printer with an RS232 interface.
3. Use the Electron interface on the BBC to feed the printer port.

Figure 10 of the article is wrong in several areas and should be replaced by new Fig. 10 shown here.

**True-RMS meter**
January 1987 p. 1.30

The correct signal assignment for the contacts on Sec is: Sec contact a = Dp 2; Sec contact b = Dp 1; Sec contact c = Dp 3.

**High power AF amplifier**
July 1986 p. 7-18

The suggested heat sink should be the Fisher Type SK33, not the SK38 as stated in the parts list.
Learn Process Control

Impact-1 is a unique combination of Hardware and Software designed for the first time in India for learning Process Control Applications. It is an 8085A based system with on-board ADC/DAC, Timer/Counter, Interrupt Controller, RS 232C Serial Port, Centronics Parallel Port, ASCII Keyboard Interface, Cassette Interface, 48 Parallel I/O lines, EPROM Programmer for 2716 to 27256 with optional Fast-Intelligent Mode, Hex Keypad with 6 digit LED display, STD Bus on card edge and six 28 pin sockets to take memory up to 64K total. Powerful Firmware is given in a 16K EPROM and is supported by comprehensive Documentation. In short, Impact-1 has everything that is required for training in process control applications and that is why it greets you with the message 'Pro Con' at power up!

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